





~~DNP~~
FID1

~~DNP~~
FID2

~~DNP~~
FID3

PCB
LOGO
Texas Instruments

[illegible]

The Label is generated with String "Board:" followed by the sequential number of the boards

LBL1

PCB Label

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2

Assembly Note

Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5

Assembly Note

Connector J2 is placed on the bottom side