

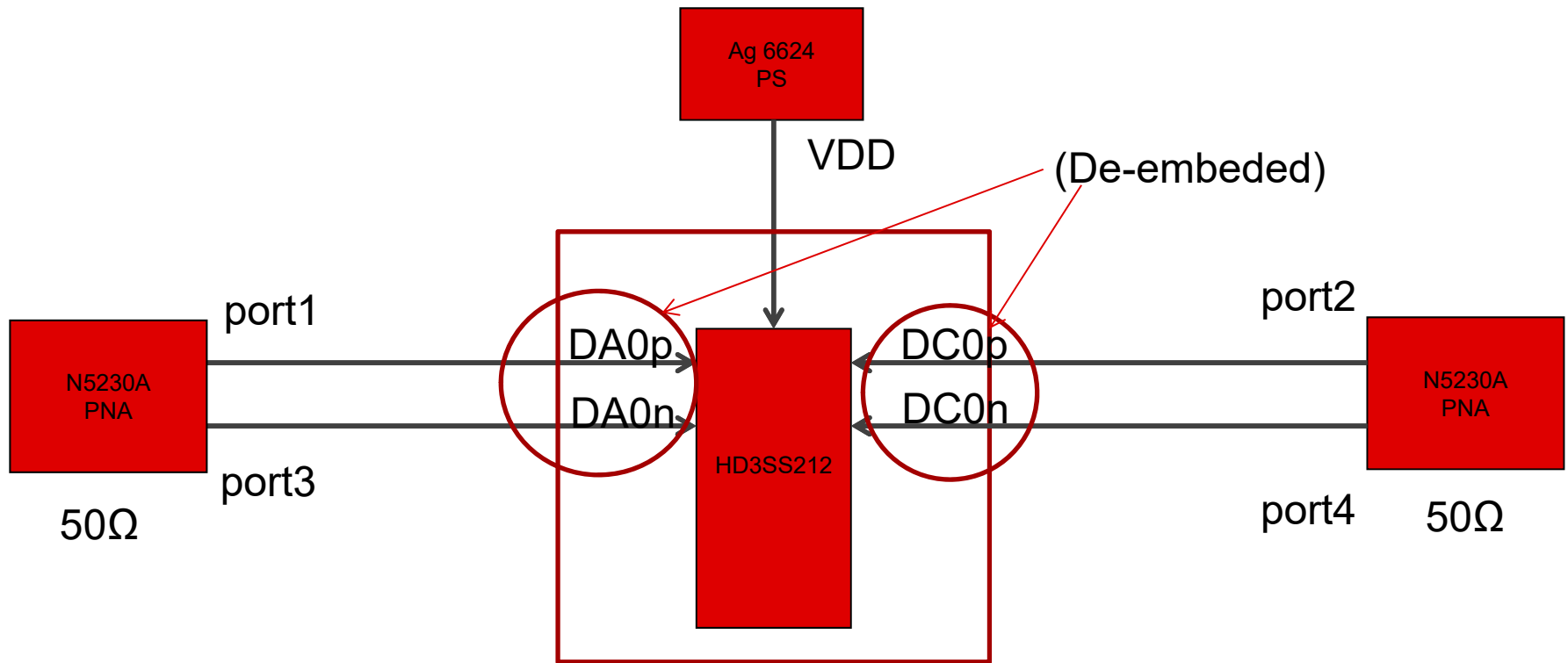
HD3SS212ZQE: S-parameter

High Speed Signal Conditioners

Setup Notes:

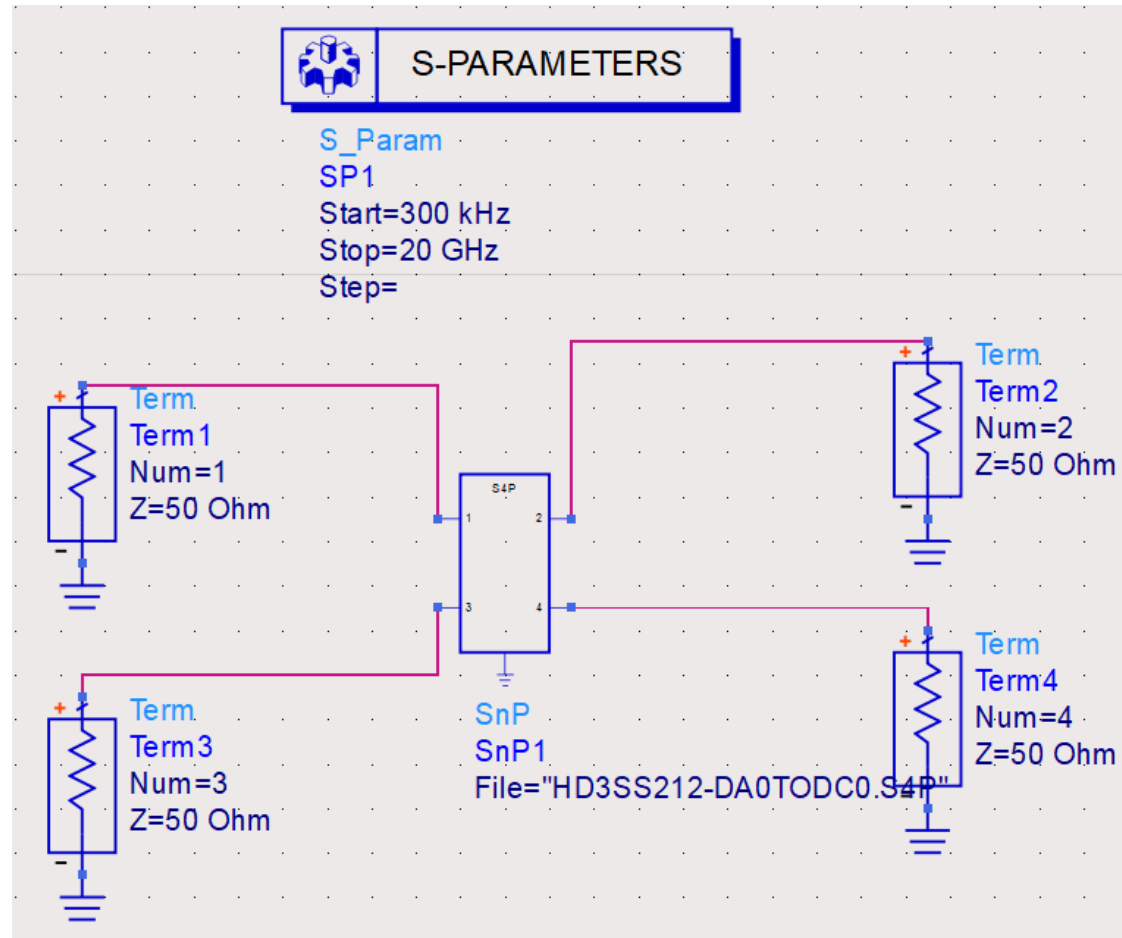
- Bench board traces have been de-embedded at the VNA (Ag N5230A)
- Using RF power of -10dBm
- VNA start freq: 300 kHz, stop freq: 20 GHz, IF BW:1kHz
- Port order: (1,3,2,4)

HD3SS212 test setup: DA0 to DC0 path



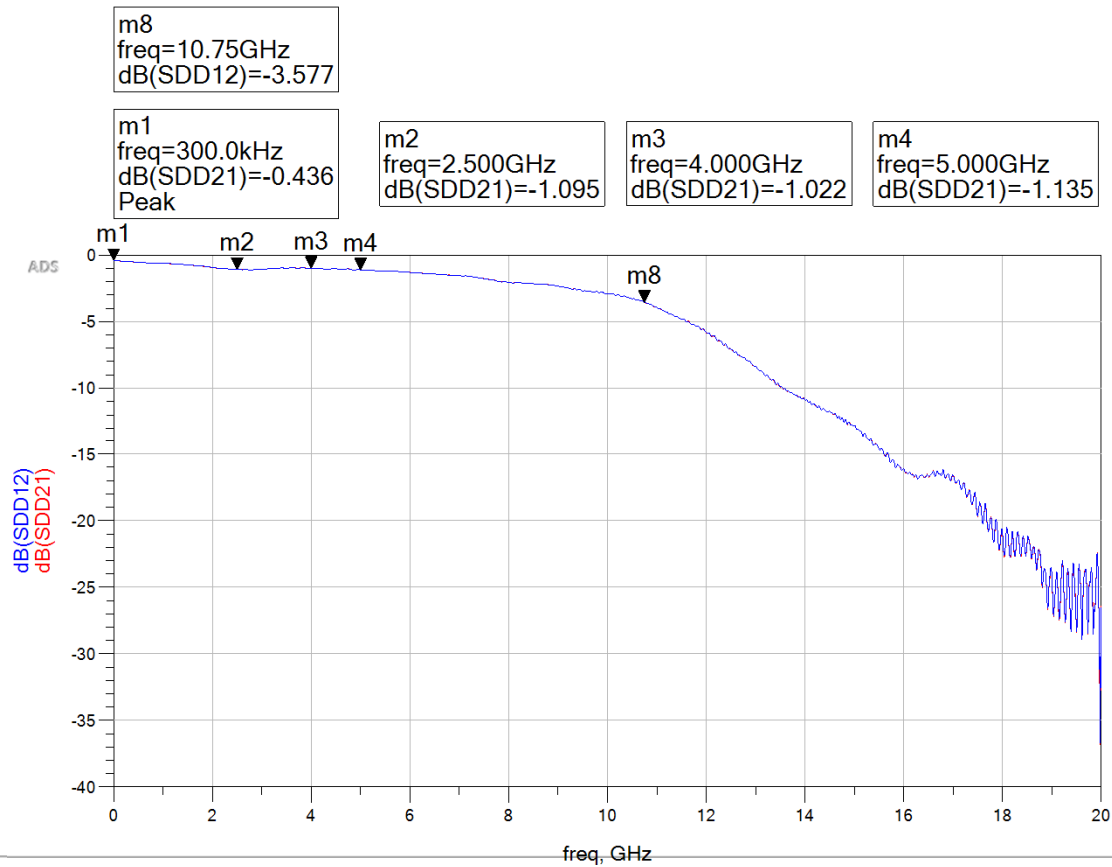
- Unused Ports terminated 50-ohms to GND.

ADS Test Bench



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DA0 to DC0: Insertion Loss



DA0 to DC0: Return Loss

m5
freq=2.500GHz
dB(SDD11)=-9.681

m6
freq=3.963GHz
dB(SDD11)=-12.907

m7
freq=5.000GHz
dB(SDD22)=-20.163

