

# DISCLAIMER

\* TS3A225E

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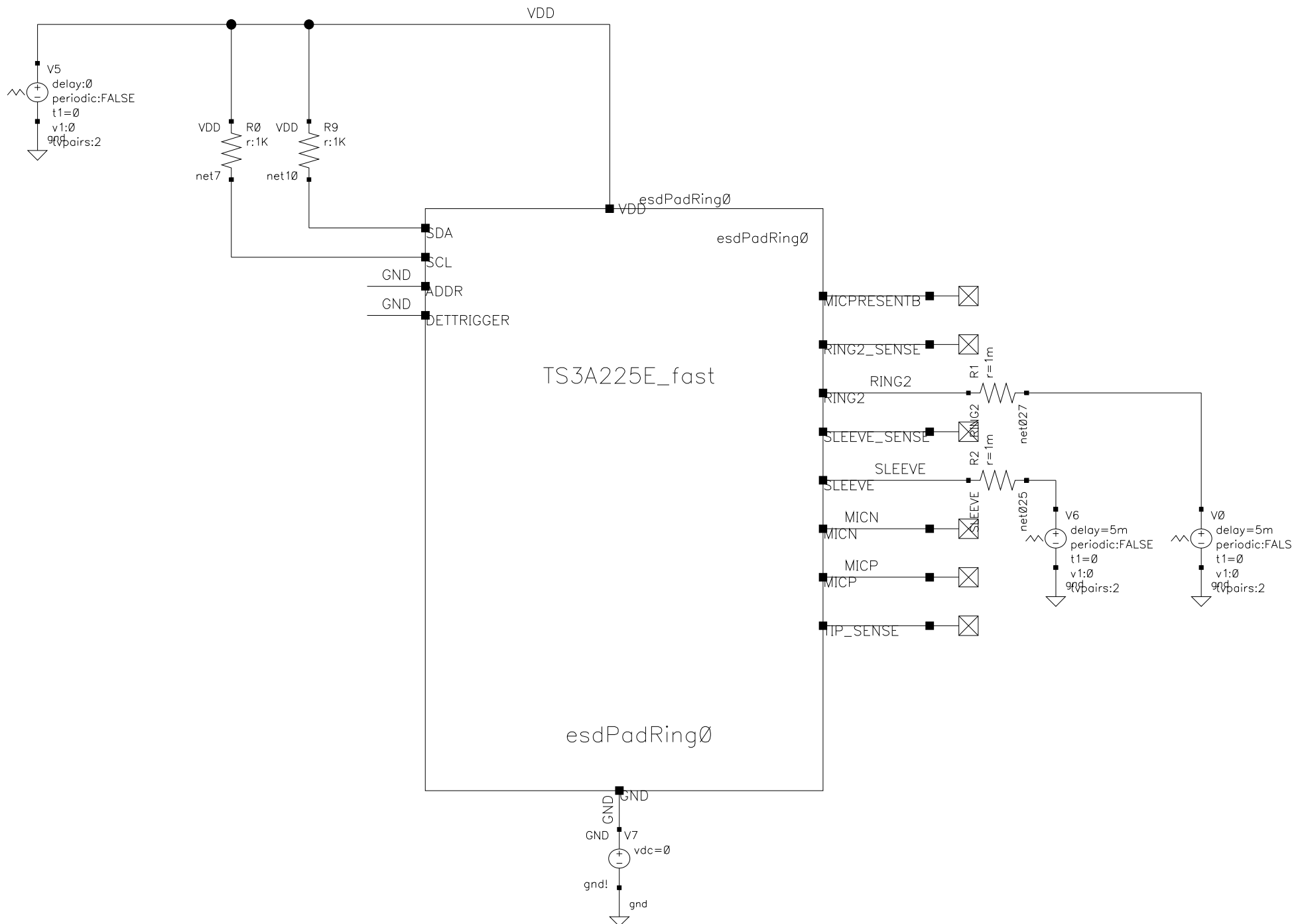
## File Description

‘TS3A225E\_fet1\_ron\_TB\_windows.sp’ -which is the test bench- invokes other 2 files:  
‘process\_models\_windows.lib’ –nominal process library, and  
‘TS3A225E\_subckts\_windows.sp’ –subcircuits’ definition.

## **Instructions**

- 1) Open your simulator HSPICE\_A-2008.03-SP1 on Windows
- 2) On the file menu, open and run file 'TS3A225E\_fet1\_ron\_TB\_windows.sp'.

Plots of the test-setup schematic and simulation results are shown below:



TS3A225E: FET rds\_on

