TPS923610 LED Driver Evaluation Module



Description

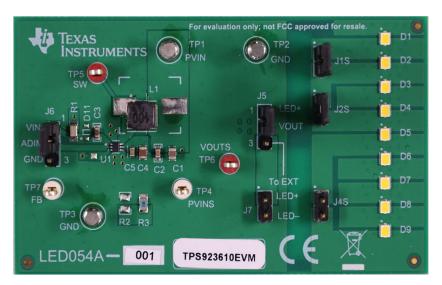
The Texas Instruments TPS923610EVM evaluation module (EVM) helps designers evaluate the operation and performance of the synchronous boost LED (light emitting diode) driver TPS923610. The TPS923610EVM operates from 2.5V to 5.5V input, 3.6V nominal, and provides default 60mA constant current to driver onboard LED string or the external LED load through the jumper.

Features

- 2.5V to 5.5V input voltage range
- 60mA constant output current
- 5V to 24.5V output voltage range
- 0.1% to 100% dimming range
- · 1.1MHz switching frequency
- Force PWM mode for low output ripple

1 Applications

- LCD back lighting
 - Smartphone
 - Thermostat
 - HMI panel
 - GPS personal navigation device
 - Dashboard camera
- · General illumination
 - IP network camera
 - Video doorbell
 - Vacuum robot



TPS923610EVM (Top View)

2 Evaluation Module Overview

2.1 Introduction

The TPS923610 is a synchronous boost LED driver operates at 400kHz or 1.1MHz switching frequency to support use of relatively small inductors for an optimized design size. The ultra-low 0.13µA current in shutdown mode can further prolong battery life. The TPS923610 can drive single or parallel LED strings for LCD backlighting and general illumination.

This user's guide contains information for the TPS923610 and support documentation for the TPS923610EVM evaluation module. This user's guide includes the performance specifications, schematic and the bill of materials of the TPS923610EVM.

2.2 Kit Contents

- One TPS923610EVM Board
- EVM disclaimer Read Me

2.3 Specification

A summary of the TPS923610EVM performance specifications is provided in Table 2-1. Specifications are given for typical input voltage 3.6V and constant output current 60mA. The ambient temperature is 25°C for all measurement, unless otherwise noted.

For applications with a different input voltage range or different output voltage and current, see the TPS923610 data sheet.

Table 2-1. Performance Specifications Summary

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Specifications	Test Conditions	MIN	TYP	MAX	Unit
Input voltage range		2.5	3.6	5.5	V
Output current set point	R_{SET} = 3.32 Ω , V_{IN} = 3.6V, 100% duty PWM input		60		mA
Output current dimming range		0.1		100	%
Operating frequency	V _{IN} = 3.6V, I _{OUT} = 60mA, 6 WLEDs in series		1100		kHz
Efficiency	V _{IN} = 3.6V, I _{OUT} = 60mA		92.1		%

2.4 Device Information

Rated input voltage and output current ranges for the evaluation module are given in Table 2-2.

Table 2-2. Input Voltage and Output Current Summary

EVM	Input Voltage (V _{IN})	Output Current (I _{OUT})	Maximum Output
	Range	Range	Voltage
TPS923610EVM	2.5V to 5.5V	0A to 60mA	24.5V

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3 Hardware

3.1 Input and Output Connections

The TPS923610EVM is provided with input and output connectors and test points as shown in Table 3-1. Figure 3-1 shows connectors and jumpers placement on the TPS923610EVM board.

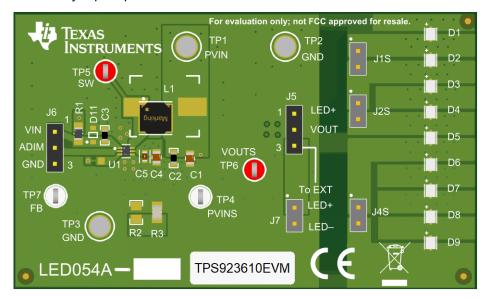


Figure 3-1. TPS923610EVM Connectors and Jumpers Placement

Table	3-1	Connec	tor and	Test	Points

Reference Designator	Function
J1S	Jumper choice to short 1 WLED of D2
J2S	Jumper choice to short 2 WLEDs of D3 and D4
J4S	Jumper choice to short 4 WLEDs of D6, D7, D8 and D9
J5	short pin1 and pin2 to drive onboard LED string short pin2 and pin3 to drive external LED load
J6	 Short pin and pin2 to enable the converter and 100% output current. short pin2 and pin3 to shutdown the converter. Open J6, apply external PWM signal on J6 pin2 (ADIM) for dimming control.
J7	Connect to external LED load, the total VF must be within 5V to 24.5V range.
TP1	PVIN positive power point
TP2, TP3	GND power point
TP4	PVIN positive sense point
TP5	SW node test point
TP6	VOUT positive sense point
TP7	FB pin voltage sense point

4 Implementation Results

4.1 Test Setup

This section describes how to properly connect, set up, and use the TPS923610EVM.

4.1.1 Start-Up Procedure

- The converter TPS923610 is enabled when the ADIM pin is pull up to VIN pin (short J6 pin1 and pin2 on EVM)
- 2. J6 pin2 is the terminal for external enable and PWM dimming signal, PWM frequency range is 10KHz to 200KHz.
- 3. A power supply capable of supplying 3A must be connected to PVIN (TP1) and GND (TP2), wires must be twisted and kept as short as possible to minimize voltage drop, inductance, and EMI transmission.
- 4. Test point TP4 provides a place to monitor the PVIN input voltages. Test point TP6 is used to monitor the output voltage.
- 5. J5 is used to select onboard or external LED load.
- 6. Open or short J1S, J2S and J4S can change the White LED numbers from 1 to 9, make sure the VOUT drop on LED load is 5V or higher for proper operating.

4.2 Output Current Setting

The FB voltage is regulated to a 200mV reference voltage. The LED current is set externally using a current-sense resistor in series with the LED strings. The value of the R_{SET} (R3 on EVM) is calculated using:

$$I_{OUT} = \frac{V_{FB}}{R_{SET}} \tag{1}$$

where

- I_{LED} = total output current of LED strings
- V_{FB} = regulated voltage of FB pin
- R_{SET} = current sense resistor

The output current tolerance depends on the FB accuracy and the current sensor resistor accuracy.

R2 is optional resistor paralleled with R3 to fine tune the R_{SET} to target value, or used to balance the total power loss on R_{SET}



5 Hardware Design Files

5.1 Schematic

Figure 5-1 is the schematic for the TPS923610EVM.

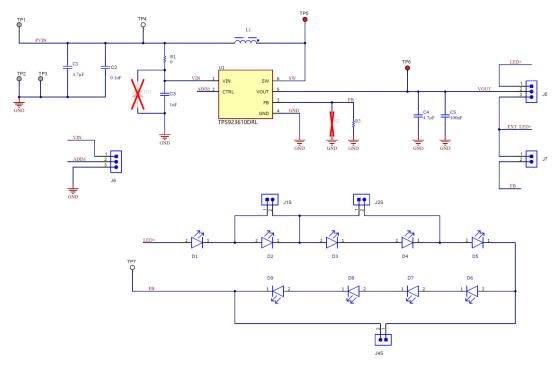


Figure 5-1. TPS923610EVM Schematic Diagram

5.2 Layout

Figure 5-2 to Figure 5-4 show the board layout for the TPS923610EVM. The top layer contains the main power traces for PVIN, VOUT, and ground. Connections for the pins of the TPS923610 and a large area filled with ground are also on the top layer. Most of the signal traces are also located on the top side. The decoupling capacitors C4, and C5 are located as close to the IC as possible. Both the top layer and bottom layer use 2oz copper thickness.

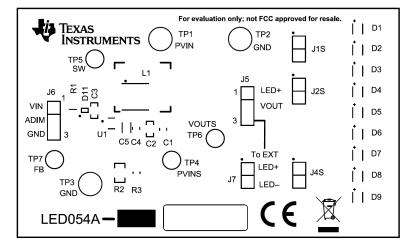


Figure 5-2. TPS923610EVM Top Assembly

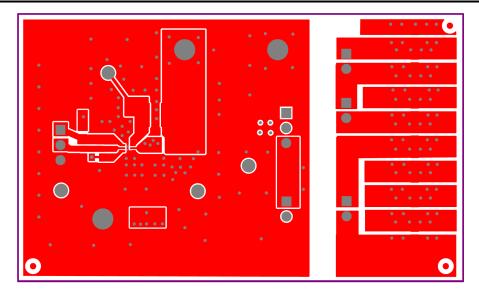


Figure 5-3. TPS923610EVM Top Layer

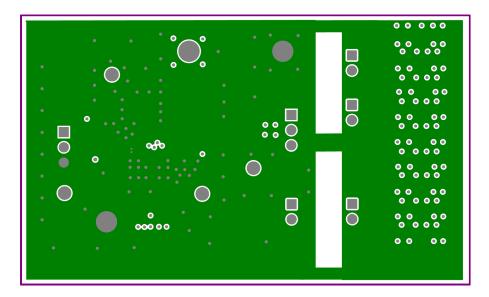


Figure 5-4. TPS923610EVM Middle Layer 1



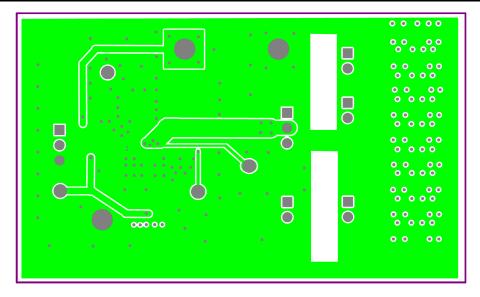


Figure 5-5. TPS923610EVM Middle Layer 2

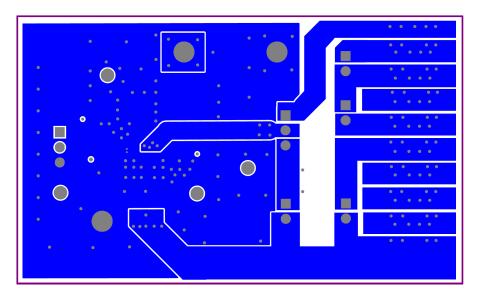


Figure 5-6. TPS923610EVM Bottom Layer

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5.3 Bill of Materials

Table 5-1. Bill of Materials

Des	Qty	Description	Part Number	Manufacturer
PCB1	1	Printed Circuit Board, 4 layers, 75mm x 45mm.	LED054A	Any
C1	1	CAP, CERM, 4.7µF, 10V, +/- 10%, X7R, 0805	GRM21BR71A475KE51L	MuRata
C2	1	CAP, CERM, 0.1µF, 10V, +/- 10%, X7R, 0603	C0603C104K8RACTU	Kemet
C3	1	CAP, CERM, 1uF, 10V, +/- 10%, X7R, 0603	GRM188R61A105KA61D	MuRata
C4	1	CAP, CERM, 4.7µF, 50V, +/- 10%, 0805	GRM21BZ71H475KE15L	MuRata
C5	1	CAP, CERM, 0.1µF, 50V, +/- 10%, 0603	C0603C104K5RACTU	Kemet
D1, D2, D3, D4, D5, D6, D7, D8, D9	9	White LED, VF 3V, 60mA, 0806	MP-2016-1100-40-80	Luminus Devices
L1	1	10uH, Shielded Drum Core, 2.1A, DCR 64mΩ	74404054100	Wurth
J1S, J2S, J4S, J7	4	Header, 100mil, 2x1, Gold, TH	PBC02SAAN	Sullins Connector Solutions
J5, J6	2	Header, 100mil, 3x1, Gold, TH	PBC03SAAN	Sullins Connector Solutions
SH-D1, SH-D2, SH-D3, SH-D4	4	Shunt, 100mil, Gold plated, Black	881545-2	TE Connectivity
R1	1	RES, 0Ω, jumper resistor, 0805	RK73Z2ATTD	KOA Speer
R3	1	RES, 3.32Ω, 1%, 0.25W, 0805	RK73H2ATTD3R32F	KOA Speer
LBL1	1	Thermal Transfer Printable Labels, 1.250" W x 0.250" H - 10,000 per roll	THT-14-423-10	Brady
TP1, TP2, TP3	3	Terminal, Turret, TH, Double	1502-2	Keystone
TP5, TP6	2	Test Point, Multipurpose, Red, TH	5010	Keystone
TP4, TP7	2	Test Point, Multipurpose, White, TH	5012	Keystone
U1	1	2.5V to 5.5V Input, Synchronous Boost LED Driver	TPS923610DRLR	Texas Instruments

6 Additional Information

6.1 Trademarks

All trademarks are the property of their respective owners.

7 References

1. Texas Instruments, TPS923610/1/2 30V Synchronous Boost LED Driver with Ultra-low Shutdown Current and 0.1%-ratio PWM Controlled Analog Dimming, data sheet

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CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

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NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

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Last updated 10/2025