

## LMT85-Q1 1.8-V, SC70, Analog Temperature Sensors

### 1 Features

- LMT85-Q1 is AEC-Q100 Qualified for Automotive Applications:
  - Device Temperature Grade 0:  $-40^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$
  - Device HBM ESD Classification Level 2
  - Device CDM ESD Classification Level C6
- Very Accurate:  $\pm 0.4^{\circ}\text{C}$  Typical
- Low 1.8-V Operation
- Average Sensor Gain of  $-8.2\text{ mV}/^{\circ}\text{C}$
- Low  $5.4\text{-}\mu\text{A}$  Quiescent Current
- Wide Temperature Range:  $-50^{\circ}\text{C}$  to  $150^{\circ}\text{C}$
- Output is Short-Circuit Protected
- Push-Pull Output With  $\pm 50\text{-}\mu\text{A}$  Drive Capability
- Footprint Compatible With the Industry-Standard LM20/19 and LM35 Temperature Sensors
- Cost-Effective Alternative to Thermistors

### 2 Applications

- Infotainment and Cluster
- Powertrain Systems
- Smoke and Heat Detectors
- Drones
- Appliances

### 3 Description

The LMT85-Q1 is a precision CMOS temperature sensor with  $\pm 0.4^{\circ}\text{C}$  typical accuracy ( $\pm 2.7^{\circ}\text{C}$  maximum) and a linear analog output voltage that is inversely proportional to temperature. The 1.8-V supply voltage operation,  $5.4\text{-}\mu\text{A}$  quiescent current, and  $0.7\text{-ms}$  power-on time enable effective power-cycling architectures to minimize power consumption for battery-powered applications such as drones and sensor nodes. The LMT85-Q1 device is AEC-Q100 Grade 0 qualified and maintains  $\pm 2.7^{\circ}\text{C}$  maximum accuracy over the full operating temperature range without calibration; this makes the LMT85-Q1 suitable for automotive applications such as infotainment, cluster, and powertrain systems. The accuracy over the wide operating range and other features make the LMT85-Q1 an excellent alternative to thermistors.

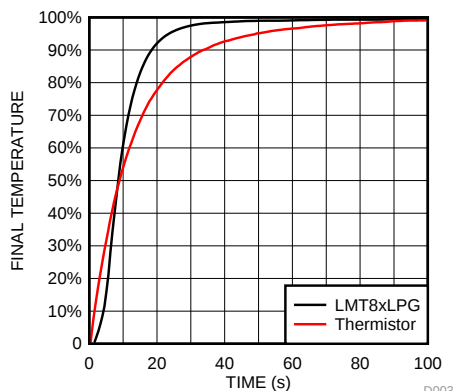
For devices with different average sensor gains and comparable accuracy, refer to [Comparable Alternative Devices](#) for alternative devices in the LMT8x family.

#### Device Information<sup>(1)</sup>

| PART NUMBER | PACKAGE | BODY SIZE (NOM)          |
|-------------|---------|--------------------------|
| LMT85-Q1    | SOT (5) | 2.00 mm $\times$ 1.25 mm |

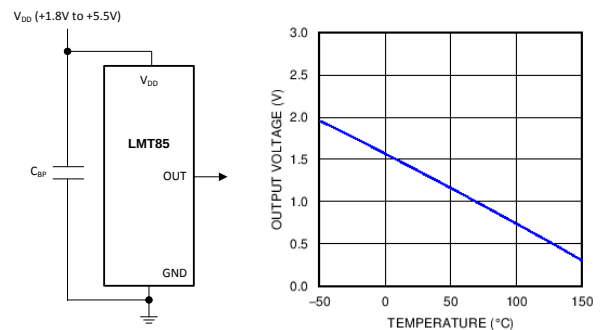
(1) For all available packages, see the orderable addendum addendum at the end of the data sheet.

**Thermal Time Constant**



\* Fast thermal response NTC

**Output Voltage vs Temperature**



Copyright © 2016, Texas Instruments Incorporated



## Table of Contents

|                                                |          |                                                                  |           |
|------------------------------------------------|----------|------------------------------------------------------------------|-----------|
| <b>1 Features</b> .....                        | <b>1</b> | 8.3 Feature Description .....                                    | <b>8</b>  |
| <b>2 Applications</b> .....                    | <b>1</b> | 8.4 Device Functional Modes .....                                | <b>10</b> |
| <b>3 Description</b> .....                     | <b>1</b> | <b>9 Application and Implementation</b> .....                    | <b>12</b> |
| <b>4 Revision History</b> .....                | <b>2</b> | 9.1 Application Information .....                                | <b>12</b> |
| <b>5 Device Comparison Tables</b> .....        | <b>3</b> | 9.2 Typical Applications .....                                   | <b>12</b> |
| <b>6 Pin Configuration and Functions</b> ..... | <b>3</b> | <b>10 Power Supply Recommendations</b> .....                     | <b>13</b> |
| <b>7 Specifications</b> .....                  | <b>4</b> | <b>11 Layout</b> .....                                           | <b>14</b> |
| 7.1 Absolute Maximum Ratings .....             | <b>4</b> | 11.1 Layout Guidelines .....                                     | <b>14</b> |
| 7.2 ESD Ratings .....                          | <b>4</b> | 11.2 Layout Example .....                                        | <b>14</b> |
| 7.3 Recommended Operating Conditions .....     | <b>4</b> | <b>12 Device and Documentation Support</b> .....                 | <b>15</b> |
| 7.4 Thermal Information .....                  | <b>4</b> | 12.1 Receiving Notification of Documentation Updates .....       | <b>15</b> |
| 7.5 Accuracy Characteristics .....             | <b>5</b> | 12.2 Community Resources .....                                   | <b>15</b> |
| 7.6 Electrical Characteristics .....           | <b>5</b> | 12.3 Trademarks .....                                            | <b>15</b> |
| 7.7 Typical Characteristics .....              | <b>6</b> | 12.4 Electrostatic Discharge Caution .....                       | <b>15</b> |
| <b>8 Detailed Description</b> .....            | <b>8</b> | 12.5 Glossary .....                                              | <b>15</b> |
| 8.1 Overview .....                             | <b>8</b> | <b>13 Mechanical, Packaging, and Orderable Information</b> ..... | <b>15</b> |
| 8.2 Functional Block Diagram .....             | <b>8</b> |                                                                  |           |

## 4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| DATE         | REVISION | NOTES                                                                                     |
|--------------|----------|-------------------------------------------------------------------------------------------|
| October 2017 | *        | Initial release. Moved the automotive device from the SNIS200 to a standalone data sheet. |

## 5 Device Comparison Tables

**Table 1. Available Device Packages**

| ORDER NUMBER <sup>(1)</sup> | PACKAGE                              | PIN | BODY SIZE (NOM)   | MOUNTING TYPE                |
|-----------------------------|--------------------------------------|-----|-------------------|------------------------------|
| LMT85DCK                    | SOT (AKA <sup>(2)</sup> : SC70, DCK) | 5   | 2.00 mm × 1.25 mm | Surface Mount                |
| LMT85LP                     | TO-92 (AKA <sup>(2)</sup> : LP)      | 3   | 4.30 mm × 3.50 mm | Through-hole; straight leads |
| LMT85LPG                    | TO-92S (AKA <sup>(2)</sup> : LPG)    | 3   | 4.00 mm × 3.15 mm | Through-hole; straight leads |
| LMT85LPM                    | TO-92 (AKA <sup>(2)</sup> : LPM)     | 3   | 4.30 mm × 3.50 mm | Through-hole; formed leads   |
| LMT85DCK-Q1                 | SOT (AKA <sup>(2)</sup> : SC70, DCK) | 5   | 2.00 mm × 1.25 mm | Surface Mount                |

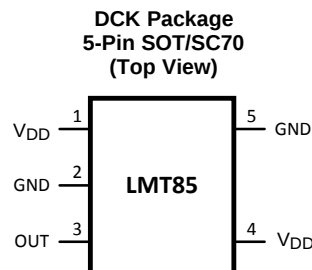
(1) For all available packages and complete order numbers, see the Package Option addendum at the end of the data sheet.

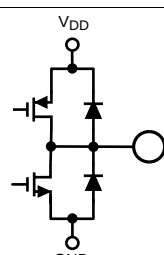
(2) AKA = Also Known As

**Table 2. Comparable Alternative Devices**

| DEVICE NAME              | AVERAGE OUTPUT SENSOR GAIN | POWER SUPPLY RANGE |
|--------------------------|----------------------------|--------------------|
| <a href="#">LMT84-Q1</a> | –5.5 mV/°C                 | 1.5 V to 5.5 V     |
| <a href="#">LMT85-Q1</a> | –8.2 mV/°C                 | 1.8 V to 5.5 V     |
| <a href="#">LMT86-Q1</a> | –10.9 mV/°C                | 2.2 V to 5.5 V     |
| <a href="#">LMT87-Q1</a> | –13.6 mV/°C                | 2.7 V to 5.5 V     |

## 6 Pin Configuration and Functions


**Pin Functions**

| PIN             |                      | TYPE          | DESCRIPTION                                                                         |                                                                 |
|-----------------|----------------------|---------------|-------------------------------------------------------------------------------------|-----------------------------------------------------------------|
| NAME            | SOT (SC70)           |               | EQUIVALENT CIRCUIT                                                                  | FUNCTION                                                        |
| GND             | 2 <sup>(1)</sup> , 5 | Ground        | N/A                                                                                 | Power Supply Ground                                             |
| OUT             | 3                    | Analog Output |  | Outputs a voltage that is inversely proportional to temperature |
| V <sub>DD</sub> | 1, 4                 | Power         | N/A                                                                                 | Positive Supply Voltage                                         |

(1) Direct connection to the back side of the die

## 7 Specifications

### 7.1 Absolute Maximum Ratings

 See <sup>(1)(2)</sup>

|                                                   | MIN  | MAX                     | UNIT |
|---------------------------------------------------|------|-------------------------|------|
| Supply voltage                                    | −0.3 | 6                       | V    |
| Voltage at output pin                             | −0.3 | (V <sub>DD</sub> + 0.5) | V    |
| Output current                                    | −7   | 7                       | mA   |
| Input current at any pin <sup>(3)</sup>           | −5   | 5                       | mA   |
| Maximum junction temperature (T <sub>JMAX</sub> ) |      | 150                     | °C   |
| Storage temperature, T <sub>stg</sub>             | −65  | 150                     | °C   |

- (1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) Soldering process must comply with Reflow Temperature Profile specifications. Refer to [www.ti.com/packaging](http://www.ti.com/packaging).
- (3) When the input voltage (V<sub>I</sub>) at any pin exceeds power supplies (V<sub>I</sub> < GND or V<sub>I</sub> > V), the current at that pin should be limited to 5 mA.

### 7.2 ESD Ratings

|                                            | VALUE                                                   | UNIT  |
|--------------------------------------------|---------------------------------------------------------|-------|
| LMT85DCK-Q1 in SC70 package                |                                                         |       |
| V <sub>(ESD)</sub> Electrostatic discharge | Human-body model (HBM), per AEC Q100-002 <sup>(1)</sup> | ±2500 |
|                                            | Charged-device model (CDM), per AEC Q100-011            | ±1000 |
|                                            |                                                         | V     |

- (1) AEC Q100-002 indicates that HBM stressing shall be in accordance with the ANSI/ESDA/JEDEC JS-001 specification.

### 7.3 Recommended Operating Conditions

|                                   | MIN                                                  | MAX | UNIT |
|-----------------------------------|------------------------------------------------------|-----|------|
| Specified temperature             | T <sub>MIN</sub> ≤ T <sub>A</sub> ≤ T <sub>MAX</sub> |     | °C   |
|                                   | −50 ≤ T <sub>A</sub> ≤ 150                           |     | °C   |
| Supply voltage (V <sub>DD</sub> ) | 1.8                                                  | 5.5 | V    |

### 7.4 Thermal Information<sup>(1)</sup>

| THERMAL METRIC <sup>(2)</sup> |                                                          | LMT85-Q1       | UNIT |
|-------------------------------|----------------------------------------------------------|----------------|------|
|                               |                                                          | DCK (SOT/SC70) |      |
|                               |                                                          | 5 PINS         |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance <sup>(3)(4)</sup> | 275            | °C/W |
| R <sub>θJC(top)</sub>         | Junction-to-case (top) thermal resistance                | 84             | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance                     | 56             | °C/W |
| ψ <sub>JT</sub>               | Junction-to-top characterization parameter               | 1.2            | °C/W |
| ψ <sub>JB</sub>               | Junction-to-board characterization parameter             | 55             | °C/W |

- (1) For information on self-heating and thermal response time, see section [Mounting and Thermal Conductivity](#).
- (2) For more information about traditional and new thermal metrics, see the [IC Package Thermal Metrics](#) application report.
- (3) The junction to ambient thermal resistance (R<sub>θJA</sub>) under natural convection is obtained in a simulation on a JEDEC-standard, High-K board as specified in JESD51-7, in an environment described in JESD51-2. Exposed pad packages assume that thermal vias are included in the PCB, per JESD 51-5.
- (4) Changes in output due to self-heating can be computed by multiplying the internal dissipation by the thermal resistance.

## 7.5 Accuracy Characteristics

These limits do not include DC load regulation. These stated accuracy limits are with reference to the values in [Table 3](#).

| PARAMETER                           | TEST CONDITIONS                                                                                       | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT               |
|-------------------------------------|-------------------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|--------------------|
| Temperature accuracy <sup>(3)</sup> | $T_A = T_J = 20^{\circ}\text{C}$ to $150^{\circ}\text{C}$ ; $V_{DD} = 1.8\text{ V}$ to $5.5\text{ V}$ | -2.7               | $\pm 0.4$          | 2.7                | $^{\circ}\text{C}$ |
|                                     | $T_A = T_J = 0^{\circ}\text{C}$ to $150^{\circ}\text{C}$ ; $V_{DD} = 1.9\text{ V}$ to $5.5\text{ V}$  | -2.7               | $\pm 0.7$          | 2.7                | $^{\circ}\text{C}$ |
|                                     | $T_A = T_J = 0^{\circ}\text{C}$ to $150^{\circ}\text{C}$ ; $V_{DD} = 2.6\text{ V}$ to $5.5\text{ V}$  |                    | $\pm 0.3$          |                    | $^{\circ}\text{C}$ |
|                                     | $T_A = T_J = -50^{\circ}\text{C}$ to $0^{\circ}\text{C}$ ; $V_{DD} = 2.3\text{ V}$ to $5.5\text{ V}$  | -2.7               | $\pm 0.7$          | 2.7                | $^{\circ}\text{C}$ |
|                                     | $T_A = T_J = -50^{\circ}\text{C}$ to $0^{\circ}\text{C}$ ; $V_{DD} = 2.9\text{ V}$ to $5.5\text{ V}$  |                    | $\pm 0.25$         |                    | $^{\circ}\text{C}$ |

- (1) Limits are specific to TI's AOQL (Average Outgoing Quality Level).
- (2) Typicals are at  $T_J = T_A = 25^{\circ}\text{C}$  and represent most likely parametric norm.
- (3) Accuracy is defined as the error between the measured and reference output voltages, tabulated in the Transfer Table at the specified conditions of supply gain setting, voltage, and temperature (expressed in  $^{\circ}\text{C}$ ). Accuracy limits include line regulation within the specified conditions. Accuracy limits do not include load regulation; they assume no DC load.

## 7.6 Electrical Characteristics

Unless otherwise noted, these specifications apply for  $V_{DD} = +1.8\text{V}$  to  $+5.5\text{V}$ . MIN and MAX limits apply for  $T_A = T_J = T_{\text{MIN}}$  to  $T_{\text{MAX}}$ , unless otherwise noted; typical values apply for  $T_A = T_J = 25^{\circ}\text{C}$ .

| PARAMETER                                            | TEST CONDITIONS                                                                                             | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT                         |
|------------------------------------------------------|-------------------------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------------------------------|
| Average sensor gain (output transfer function slope) | $-30^{\circ}\text{C}$ and $90^{\circ}\text{C}$ used to calculate average sensor gain                        |                    | -8.2               |                    | $\text{mV}/^{\circ}\text{C}$ |
| Load regulation <sup>(3)</sup>                       | Source $\leq 50\text{ }\mu\text{A}$ , $(V_{DD} - V_{\text{OUT}}) \geq 200\text{ mV}$                        | -1                 | -0.22              |                    | $\text{mV}$                  |
|                                                      | Sink $\leq 50\text{ }\mu\text{A}$ , $V_{\text{OUT}} \geq 200\text{ mV}$                                     |                    | 0.26               | 1                  | $\text{mV}$                  |
| Line regulation <sup>(4)</sup>                       |                                                                                                             |                    | 200                |                    | $\mu\text{V}/\text{V}$       |
| $I_S$ Supply current                                 | $T_A = T_J = 30^{\circ}\text{C}$ to $150^{\circ}\text{C}$ , $(V_{DD} - V_{\text{OUT}}) \geq 100\text{ mV}$  |                    | 5.4                | 8.1                | $\mu\text{A}$                |
|                                                      | $T_A = T_J = -50^{\circ}\text{C}$ to $150^{\circ}\text{C}$ , $(V_{DD} - V_{\text{OUT}}) \geq 100\text{ mV}$ |                    | 5.4                | 9                  | $\mu\text{A}$                |
| $C_L$ Output load capacitance                        |                                                                                                             |                    | 1100               |                    | $\text{pF}$                  |
| Power-on time <sup>(5)</sup>                         | $C_L = 0\text{ pF}$ to $1100\text{ pF}$                                                                     |                    | 0.7                | 1.9                | $\text{ms}$                  |
| Output drive                                         | $T_A = T_J = 25^{\circ}\text{C}$                                                                            | -50                |                    | +50                | $\mu\text{A}$                |

- (1) Limits are specific to TI's AOQL (Average Outgoing Quality Level).
- (2) Typicals are at  $T_J = T_A = 25^{\circ}\text{C}$  and represent most likely parametric norm.
- (3) Source currents are flowing out of the LMT85-Q1. Sink currents are flowing into the LMT85-Q1.
- (4) Line regulation (DC) is calculated by subtracting the output voltage at the highest supply voltage from the output voltage at the lowest supply voltage. The typical DC line regulation specification does not include the output voltage shift discussed in [Output Voltage Shift](#).
- (5) Specified by design and characterization.

## 7.7 Typical Characteristics

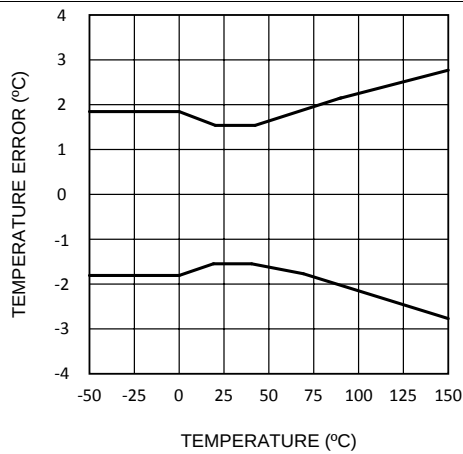


Figure 1. Temperature Error vs Temperature

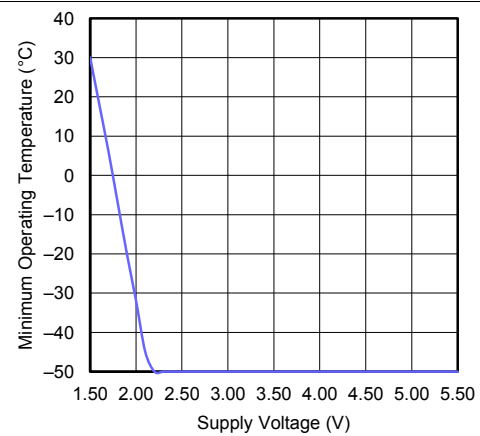


Figure 2. Minimum Operating Temperature vs Supply Voltage

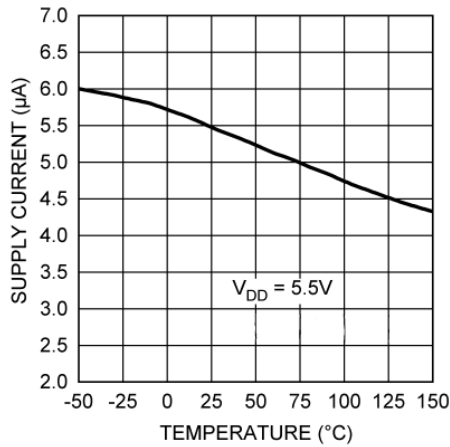


Figure 3. Supply Current vs Temperature

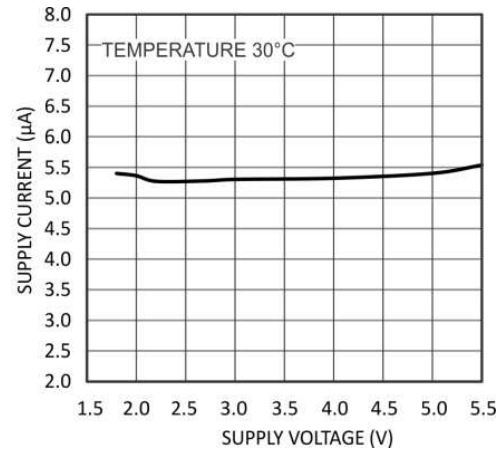


Figure 4. Supply Current vs Supply Voltage

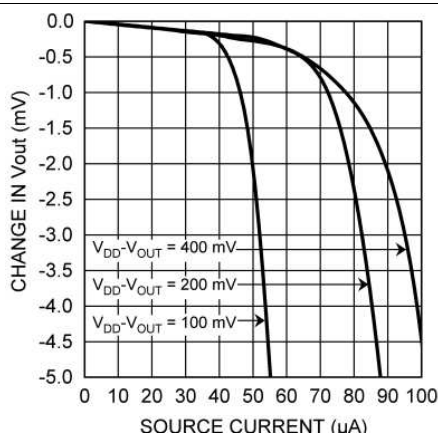


Figure 5. Load Regulation, Sourcing Current

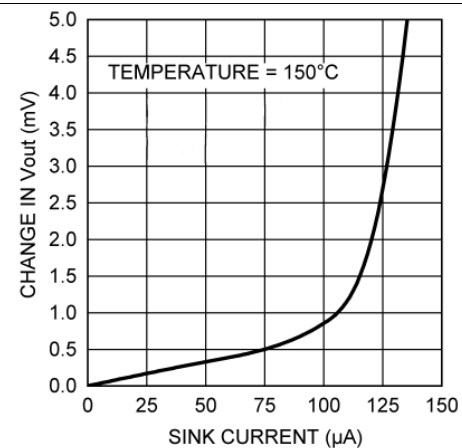


Figure 6. Load Regulation, Sinking Current

## Typical Characteristics (continued)

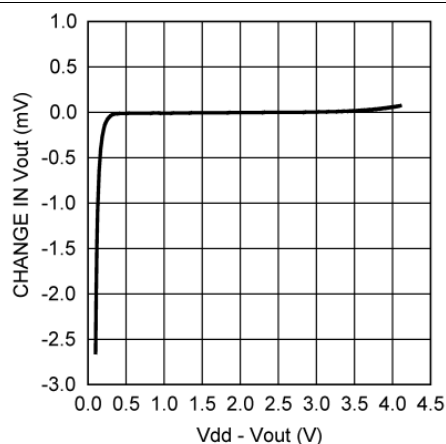


Figure 7. Change in Vout vs Overhead Voltage

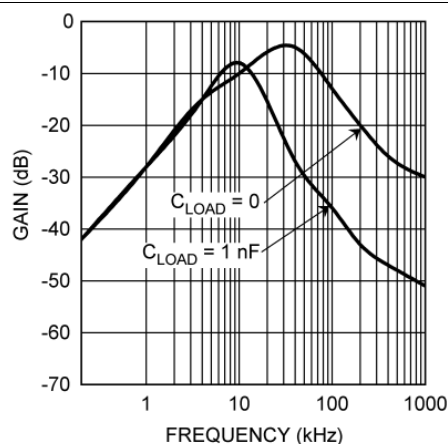


Figure 8. Supply-Noise Gain vs Frequency

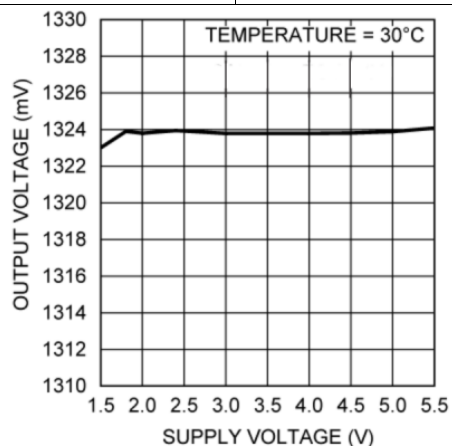


Figure 9. Output Voltage vs Supply Voltage

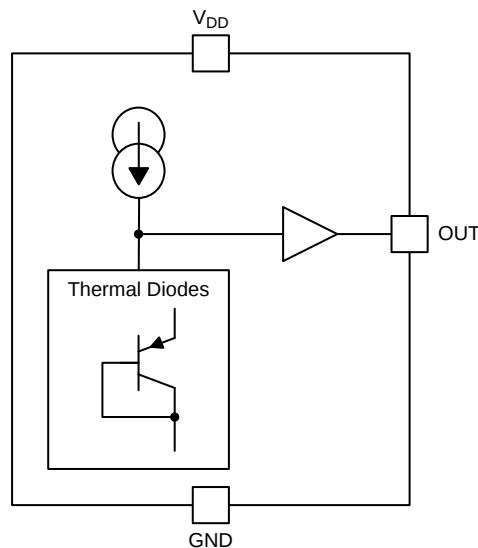
## 8 Detailed Description

### 8.1 Overview

The LMT85-Q1 is an analog output temperature sensor. The temperature sensing element is comprised of a simple base emitter junction that is forward biased by a current source. The temperature-sensing element is then buffered by an amplifier and provided to the OUT pin. The amplifier has a simple push-pull output stage thus providing a low impedance output source.

### 8.2 Functional Block Diagram

Full-Range Celsius Temperature Sensor (–50°C to 150°C).



### 8.3 Feature Description

#### 8.3.1 LMT85-Q1 Transfer Function

The output voltage of the LMT85-Q1, across the complete operating temperature range, is shown in [Table 3](#). This table is the reference from which the LMT85 accuracy specifications (listed in the [Accuracy Characteristics](#) section) are determined. This table can be used, for example, in a host processor look-up table. A file containing this data is available for download at the [LMT85-Q1](#) product folder under *Tools and Software Models*.

**Table 3. LMT85-Q1 Transfer Table**

| TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) |
|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|
| -50          | 1955                     | -10          | 1648                     | 30           | 1324                     | 70           | 991                      | 110          | 651                      |
| -49          | 1949                     | -9           | 1639                     | 31           | 1316                     | 71           | 983                      | 111          | 642                      |
| -48          | 1942                     | -8           | 1631                     | 32           | 1308                     | 72           | 974                      | 112          | 634                      |
| -47          | 1935                     | -7           | 1623                     | 33           | 1299                     | 73           | 966                      | 113          | 625                      |
| -46          | 1928                     | -6           | 1615                     | 34           | 1291                     | 74           | 957                      | 114          | 617                      |
| -45          | 1921                     | -5           | 1607                     | 35           | 1283                     | 75           | 949                      | 115          | 608                      |
| -44          | 1915                     | -4           | 1599                     | 36           | 1275                     | 76           | 941                      | 116          | 599                      |
| -43          | 1908                     | -3           | 1591                     | 37           | 1267                     | 77           | 932                      | 117          | 591                      |
| -42          | 1900                     | -2           | 1583                     | 38           | 1258                     | 78           | 924                      | 118          | 582                      |
| -41          | 1892                     | -1           | 1575                     | 39           | 1250                     | 79           | 915                      | 119          | 573                      |
| -40          | 1885                     | 0            | 1567                     | 40           | 1242                     | 80           | 907                      | 120          | 565                      |
| -39          | 1877                     | 1            | 1559                     | 41           | 1234                     | 81           | 898                      | 121          | 556                      |
| -38          | 1869                     | 2            | 1551                     | 42           | 1225                     | 82           | 890                      | 122          | 547                      |

## Feature Description (continued)

**Table 3. LMT85-Q1 Transfer Table (continued)**

| TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) | TEMP<br>(°C) | V <sub>OUT</sub><br>(mV) |
|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|--------------|--------------------------|
| -37          | 1861                     | 3            | 1543                     | 43           | 1217                     | 83           | 881                      | 123          | 539                      |
| -36          | 1853                     | 4            | 1535                     | 44           | 1209                     | 84           | 873                      | 124          | 530                      |
| -35          | 1845                     | 5            | 1527                     | 45           | 1201                     | 85           | 865                      | 125          | 521                      |
| -34          | 1838                     | 6            | 1519                     | 46           | 1192                     | 86           | 856                      | 126          | 513                      |
| -33          | 1830                     | 7            | 1511                     | 47           | 1184                     | 87           | 848                      | 127          | 504                      |
| -32          | 1822                     | 8            | 1502                     | 48           | 1176                     | 88           | 839                      | 128          | 495                      |
| -31          | 1814                     | 9            | 1494                     | 49           | 1167                     | 89           | 831                      | 129          | 487                      |
| -30          | 1806                     | 10           | 1486                     | 50           | 1159                     | 90           | 822                      | 130          | 478                      |
| -29          | 1798                     | 11           | 1478                     | 51           | 1151                     | 91           | 814                      | 131          | 469                      |
| -28          | 1790                     | 12           | 1470                     | 52           | 1143                     | 92           | 805                      | 132          | 460                      |
| -27          | 1783                     | 13           | 1462                     | 53           | 1134                     | 93           | 797                      | 133          | 452                      |
| -26          | 1775                     | 14           | 1454                     | 54           | 1126                     | 94           | 788                      | 134          | 443                      |
| -25          | 1767                     | 15           | 1446                     | 55           | 1118                     | 95           | 779                      | 135          | 434                      |
| -24          | 1759                     | 16           | 1438                     | 56           | 1109                     | 96           | 771                      | 136          | 425                      |
| -23          | 1751                     | 17           | 1430                     | 57           | 1101                     | 97           | 762                      | 137          | 416                      |
| -22          | 1743                     | 18           | 1421                     | 58           | 1093                     | 98           | 754                      | 138          | 408                      |
| -21          | 1735                     | 19           | 1413                     | 59           | 1084                     | 99           | 745                      | 139          | 399                      |
| -20          | 1727                     | 20           | 1405                     | 60           | 1076                     | 100          | 737                      | 140          | 390                      |
| -19          | 1719                     | 21           | 1397                     | 61           | 1067                     | 101          | 728                      | 141          | 381                      |
| -18          | 1711                     | 22           | 1389                     | 62           | 1059                     | 102          | 720                      | 142          | 372                      |
| -17          | 1703                     | 23           | 1381                     | 63           | 1051                     | 103          | 711                      | 143          | 363                      |
| -16          | 1695                     | 24           | 1373                     | 64           | 1042                     | 104          | 702                      | 144          | 354                      |
| -15          | 1687                     | 25           | 1365                     | 65           | 1034                     | 105          | 694                      | 145          | 346                      |
| -14          | 1679                     | 26           | 1356                     | 66           | 1025                     | 106          | 685                      | 146          | 337                      |
| -13          | 1671                     | 27           | 1348                     | 67           | 1017                     | 107          | 677                      | 147          | 328                      |
| -12          | 1663                     | 28           | 1340                     | 68           | 1008                     | 108          | 668                      | 148          | 319                      |
| -11          | 1656                     | 29           | 1332                     | 69           | 1000                     | 109          | 660                      | 149          | 310                      |
|              |                          |              |                          |              |                          |              |                          | 150          | 301                      |

Although the LMT85-Q1 is very linear, its response does have a slight umbrella parabolic shape. This shape is very accurately reflected in [Table 3](#). The Transfer Table can be calculated by using the parabolic equation ([Equation 1](#)).

$$V_{TEMP} (mV) = 1324.0mV - \left[ 8.194 \frac{mV}{^{\circ}C} (T - 30^{\circ}C) \right] - \left[ 0.00262 \frac{mV}{^{\circ}C^2} (T - 30^{\circ}C)^2 \right] \quad (1)$$

The parabolic equation is an approximation of the transfer table and the accuracy of the equation degrades slightly at the temperature range extremes. [Equation 1](#) can be solved for T resulting in:

$$T = \frac{8.194 - \sqrt{(-8.194)^2 + 4 \times 0.00262 \times (1324 - V_{TEMP} (mV))}}{2 \times -0.00262} + 30 \quad (2)$$

For an even less accurate linear transfer function approximation, a line can easily be calculated over the desired temperature range using values from the Table and a two-point equation ([Equation 3](#)):

$$V - V_1 = \left( \frac{V_2 - V_1}{T_2 - T_1} \right) \times (T - T_1)$$

where

- V is in mV,
  - T is in °C,
  - T<sub>1</sub> and V<sub>1</sub> are the coordinates of the lowest temperature,
  - and T<sub>2</sub> and V<sub>2</sub> are the coordinates of the highest temperature.
- (3)

For example, if the user wanted to resolve this equation, over a temperature range of 20°C to 50°C, they would proceed as follows:

$$V - 1405 \text{ mV} = \left( \frac{1159 \text{ mV} - 1405 \text{ mV}}{50^\circ\text{C} - 20^\circ\text{C}} \right) \times (T - 20^\circ\text{C}) \quad (4)$$

$$V - 1405 \text{ mV} = (-8.20 \text{ mV} / ^\circ\text{C}) \times (T - 20^\circ\text{C}) \quad (5)$$

$$V = (-8.20 \text{ mV} / ^\circ\text{C}) \times T + 1569 \text{ mV} \quad (6)$$

Using this method of linear approximation, the transfer function can be approximated for one or more temperature ranges of interest.

## 8.4 Device Functional Modes

### 8.4.1 Mounting and Thermal Conductivity

The LMT85-Q1 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface.

To ensure good thermal conductivity, the backside of the LMT85 die is directly attached to the GND pin. The temperatures of the lands and traces to the other leads of the LMT85-Q1 will also affect the temperature reading.

Alternatively, the LMT85-Q1 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LMT85-Q1 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. If moisture creates a short circuit from the output to ground or  $V_{DD}$ , the output from the LMT85 will not be correct. Printed-circuit coatings are often used to ensure that moisture cannot corrode the leads or circuit traces.

The thermal resistance junction to ambient ( $R_{\theta JA}$  or  $\theta_{JA}$ ) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. Use [Equation 7](#) to calculate the rise in the LMT85-Q1 die temperature:

$$T_J = T_A + \theta_{JA} [(V_{DD} I_S) + (V_{DD} - V_{OUT}) I_L]$$

where

- $T_A$  is the ambient temperature,
  - $I_S$  is the supply current,
  - $I_L$  is the load current on the output,
  - and  $V_O$  is the output voltage.
- (7)

For example, in an application where  $T_A = 30^\circ\text{C}$ ,  $V_{DD} = 5 \text{ V}$ ,  $I_S = 5.4 \mu\text{A}$ ,  $V_{OUT} = 1324 \text{ mV}$ , and  $I_L = 2 \mu\text{A}$ , the junction temperature would be  $30.014^\circ\text{C}$ , showing a self-heating error of only  $0.014^\circ\text{C}$ . Because the junction temperature of the LMT85-Q1 is the actual temperature being measured, take care to minimize the load current that the LMT85-Q1 is required to drive. [Thermal Information](#) shows the thermal resistance of the LMT85-Q1.

### 8.4.2 Output and Noise Considerations

A push-pull output gives the LMT85-Q1 the ability to sink and source significant current. This is beneficial when, for example, driving dynamic loads like an input stage on an analog-to-digital converter (ADC). In these applications the source current is required to quickly charge the input capacitor of the ADC. The LMT85-Q1 device is ideal for this and other applications which require strong source or sink current.

The LMT85-Q1 supply-noise gain (the ratio of the AC signal on  $V_{OUT}$  to the AC signal on  $V_{DD}$ ) was measured during bench tests. The typical attenuation is shown in [Figure 8](#) found in the [Typical Characteristics](#). A load capacitor on the output can help to filter noise.

For operation in very noisy environments, some bypass capacitance should be present on the supply within approximately 5 centimeters of the LMT85-Q1.

## Device Functional Modes (continued)

### 8.4.3 Capacitive Loads

The LMT85-Q1 handles capacitive loading well. In an extremely noisy environment, or when driving a switched sampling input on an ADC, it may be necessary to add some filtering to minimize noise coupling. Without any precautions, the LMT85-Q1 can drive a capacitive load less than or equal to 1100 pF as shown in Figure 10. For capacitive loads greater than 1100 pF, a series resistor may be required on the output, as shown in Figure 11.

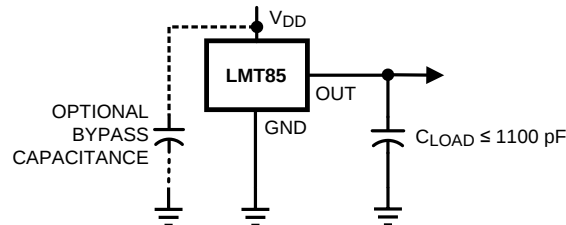


Figure 10. LMT85-Q1 No Decoupling Required for Capacitive Loads Less Than 1100 pF

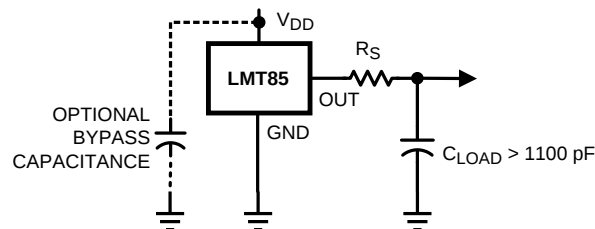


Figure 11. LMT85-Q1 with Series Resistor for Capacitive Loading Greater Than 1100 pF

Table 4. Recommended Series Resistor Values

| $C_{LOAD}$       | MINIMUM $R_S$  |
|------------------|----------------|
| 1.1 nF to 99 nF  | 3 k $\Omega$   |
| 100 nF to 999 nF | 1.5 k $\Omega$ |
| 1 $\mu$ F        | 800 $\Omega$   |

### 8.4.4 Output Voltage Shift

The LMT85-Q1 is very linear over temperature and supply voltage range. Due to the intrinsic behavior of an NMOS/PMOS rail-to-rail buffer, a slight shift in the output can occur when the supply voltage is ramped over the operating range of the device. The location of the shift is determined by the relative levels of  $V_{DD}$  and  $V_{OUT}$ . The shift typically occurs when  $V_{DD} - V_{OUT} = 1$  V.

This slight shift (a few millivolts) takes place over a wide change (approximately 200 mV) in  $V_{DD}$  or  $V_{OUT}$ . Because the shift takes place over a wide temperature change of 5°C to 20°C,  $V_{OUT}$  is always monotonic. The accuracy specifications in the [Accuracy Characteristics](#) table already include this possible shift.

## 9 Application and Implementation

### NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 9.1 Application Information

The LMT85-Q1 features make it suitable for many general temperature-sensing applications. It can operate down to 1.8-V supply with 5.4- $\mu$ A power consumption, making it ideal for battery powered devices.

### 9.2 Typical Applications

#### 9.2.1 Connection to an ADC

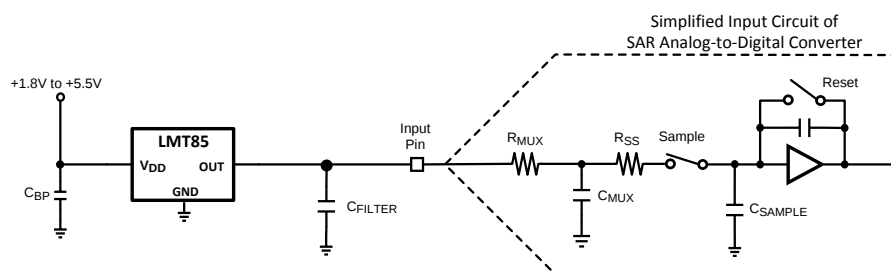


Figure 12. Suggested Connection to a Sampling Analog-to-Digital Converter Input Stage

##### 9.2.1.1 Design Requirements

Most CMOS ADCs found in microcontrollers and ASICs have a sampled data comparator input structure. When the ADC charges the sampling cap, it requires instantaneous charge from the output of the analog source such as the LMT85 temperature sensor and many op amps. This requirement is easily accommodated by the addition of a capacitor ( $C_{\text{FILTER}}$ ).

##### 9.2.1.2 Detailed Design Procedure

The size of  $C_{\text{FILTER}}$  depends on the size of the sampling capacitor and the sampling frequency. Because not all ADCs have identical input stages, the charge requirements will vary. This general ADC application is shown as an example only.

##### 9.2.1.3 Application Curve

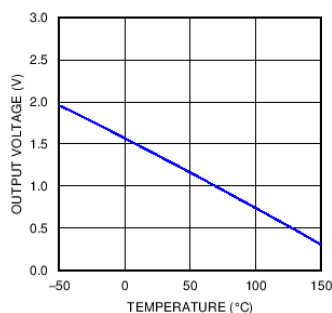


Figure 13. Analog Output Transfer Function

## Typical Applications (continued)

### 9.2.2 Conserving Power Dissipation With Shutdown

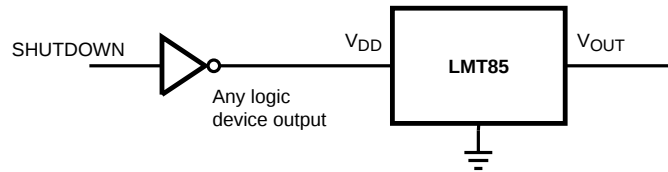


Figure 14. Simple Shutdown Connection of the LMT85-Q1

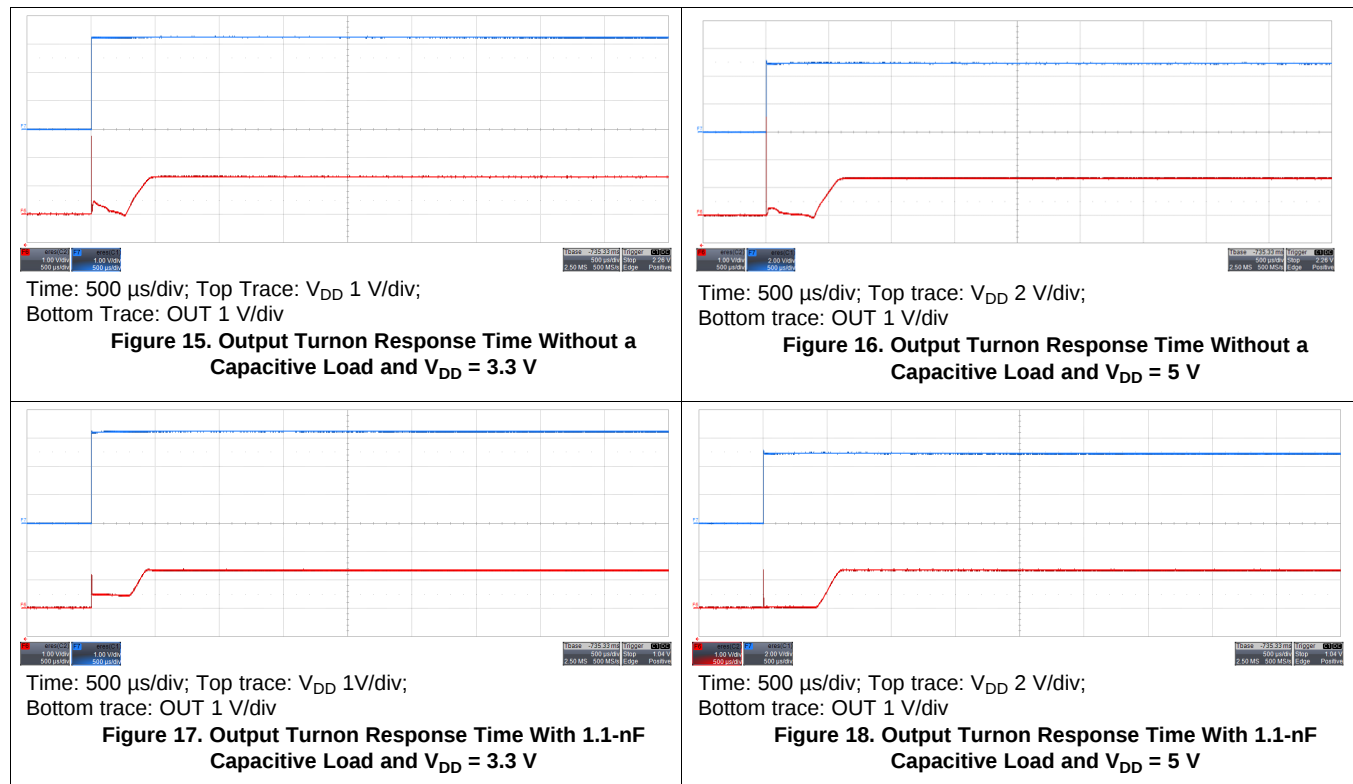
#### 9.2.2.1 Design Requirements

Because the power consumption of the LMT85-Q1 is less than 9  $\mu\text{A}$ , it can simply be powered directly from any logic gate output and therefore not require a specific shutdown pin. The device can even be powered directly from a micro controller GPIO. In this way, it can easily be turned off for cases such as battery-powered systems where power savings are critical.

#### 9.2.2.2 Detailed Design Procedure

Simply connect the  $V_{DD}$  pin of the LMT85-Q1 directly to the logic shutdown signal from a microcontroller.

#### 9.2.2.3 Application Curves



## 10 Power Supply Recommendations

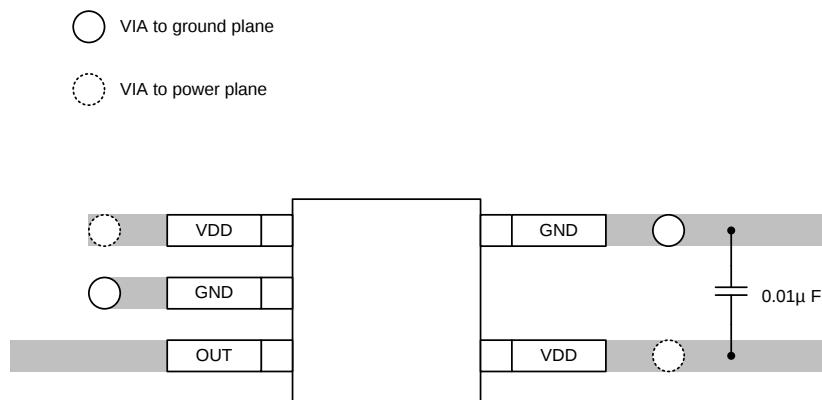
The low supply current and supply range (1.8 V to 5.5 V) of the LMT85-Q1 allow the device to easily be powered from many sources. Power supply bypassing is optional and is mainly dependent on the noise on the power supply used. In noisy systems it may be necessary to add bypass capacitors to lower the noise that is coupled to the output of the LMT85-Q1.

## 11 Layout

### 11.1 Layout Guidelines

The LMT85-Q1 is extremely simple to layout. If a power-supply bypass capacitor is used, it should be connected as shown in the [Layout Example](#)

### 11.2 Layout Example



**Figure 19. SC70 Package Recommended Layout**

## 12 Device and Documentation Support

### 12.1 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on *Alert me* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 12.2 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

**TI E2E™ Online Community** *TI's Engineer-to-Engineer (E2E) Community*. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

### 12.3 Trademarks

E2E is a trademark of Texas Instruments.  
All other trademarks are the property of their respective owners.

### 12.4 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### 12.5 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

## 13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

## PACKAGING INFORMATION

| Orderable part number        | Status<br>(1) | Material type<br>(2) | Package   Pins | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6) |
|------------------------------|---------------|----------------------|----------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|---------------------|
| <a href="#">LMT85QDCKRQ1</a> | Active        | Production           | SC70 (DCK)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -50 to 150   | BRA                 |
| LMT85QDCKRQ1.A               | Active        | Production           | SC70 (DCK)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -50 to 150   | BRA                 |
| LMT85QDCKRQ1.B               | Active        | Production           | SC70 (DCK)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -50 to 150   | BRA                 |
| <a href="#">LMT85QDCKTQ1</a> | Obsolete      | Production           | SC70 (DCK)   5 | -                     | -           | Call TI                              | Call TI                           | -50 to 150   | BRA                 |

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "-" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

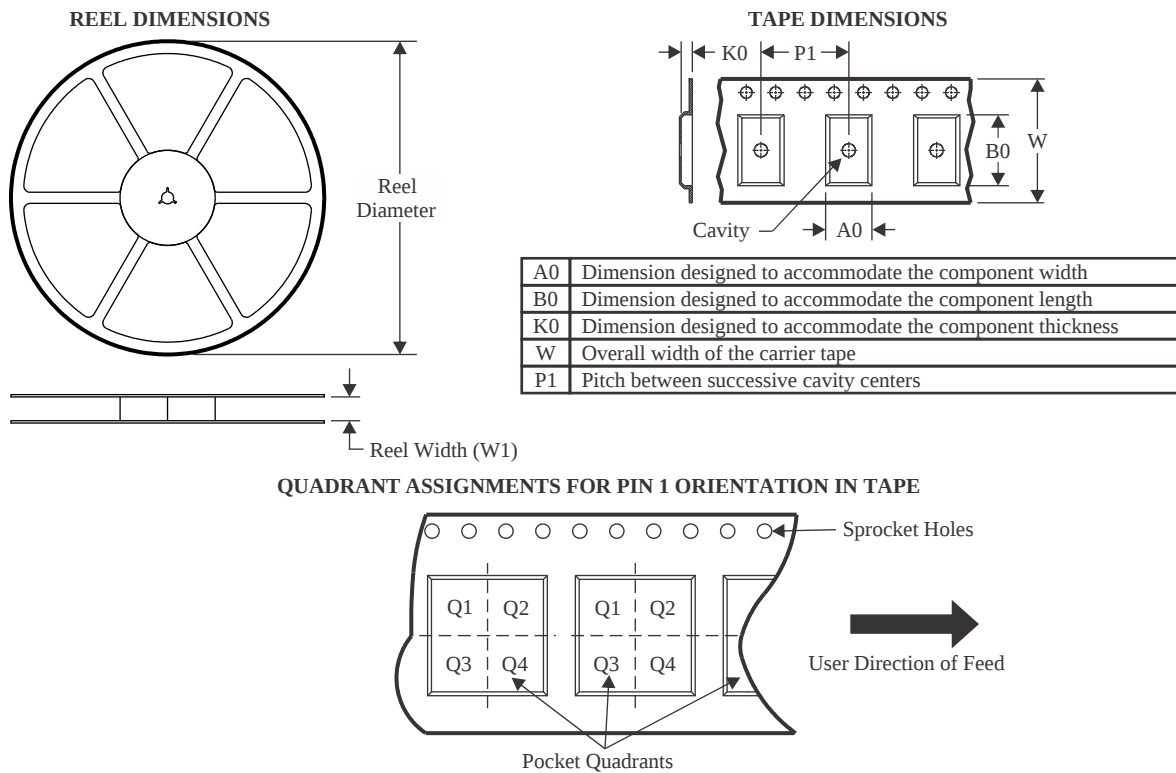
### OTHER QUALIFIED VERSIONS OF LMT85-Q1 :

- Catalog : [LMT85](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product

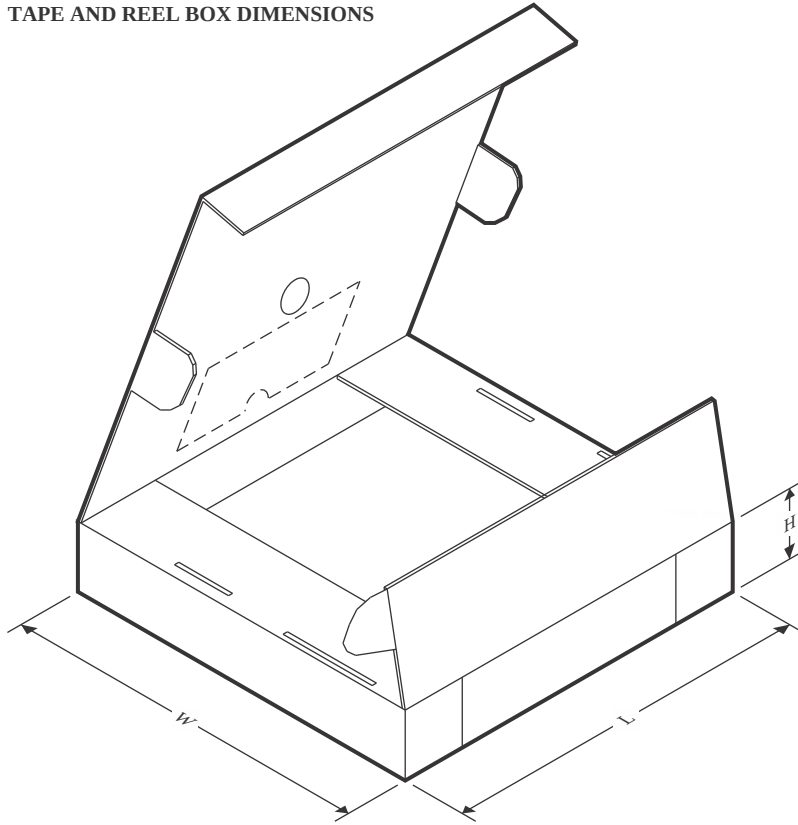
## TAPE AND REEL INFORMATION



\*All dimensions are nominal

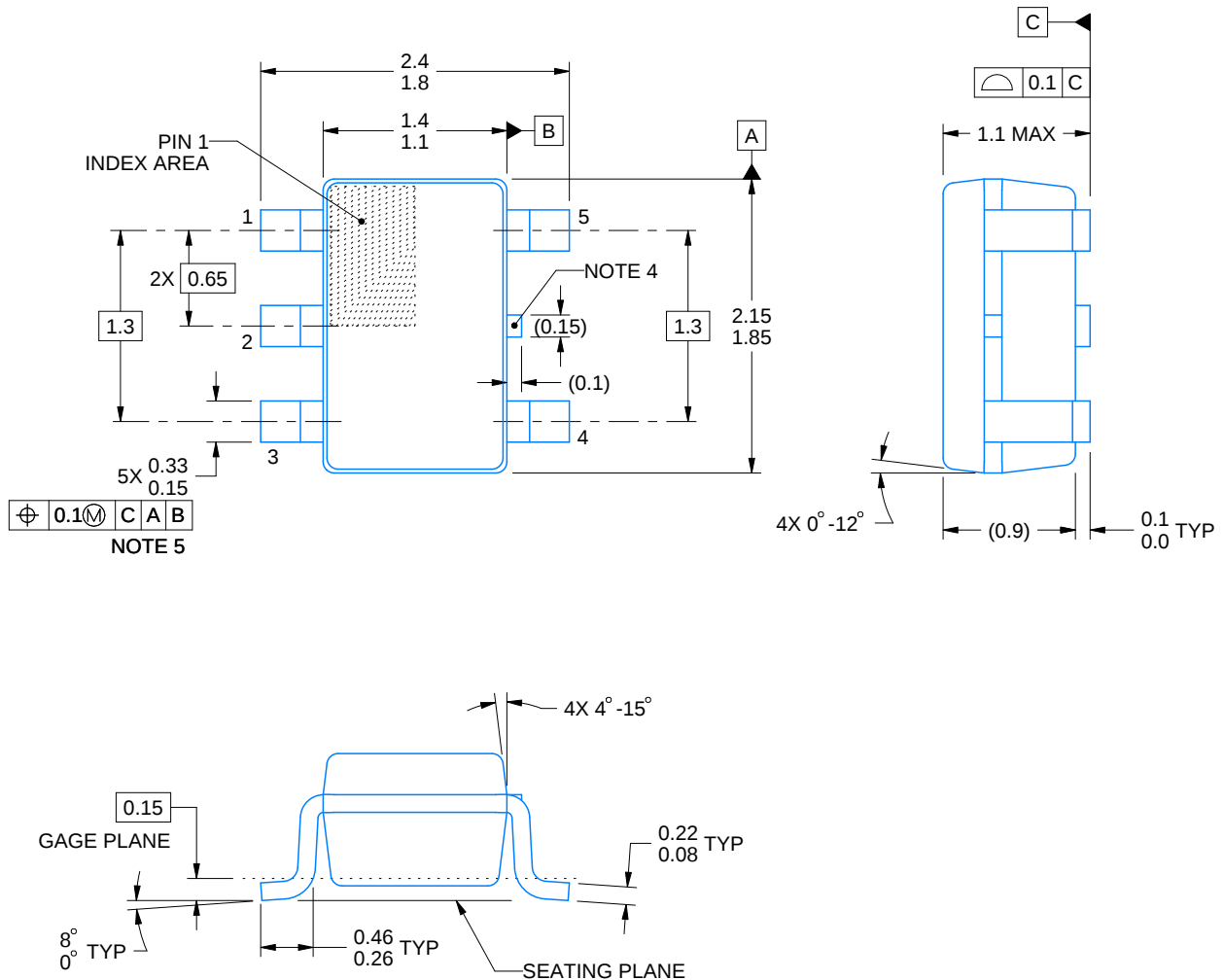
| Device       | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|--------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| LMT85QDCKRQ1 | SC70         | DCR             | 5    | 3000 | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device       | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|--------------|--------------|-----------------|------|------|-------------|------------|-------------|
| LMT85QDCKRQ1 | SC70         | DCK             | 5    | 3000 | 208.0       | 191.0      | 35.0        |



4214834/G 11/2024

## NOTES:

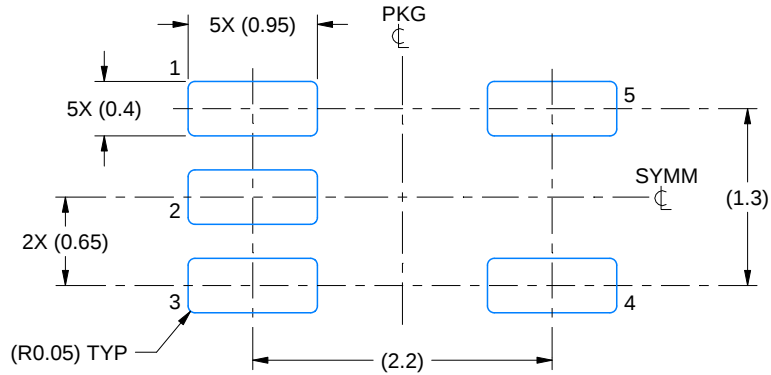
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-203.
4. Support pin may differ or may not be present.
5. Lead width does not comply with JEDEC.
6. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25mm per side

# EXAMPLE BOARD LAYOUT

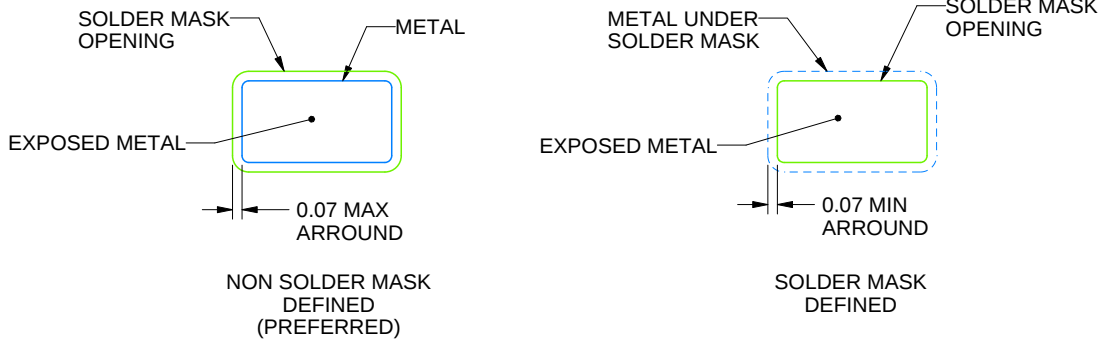
DCK0005A

SOT - 1.1 max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:18X

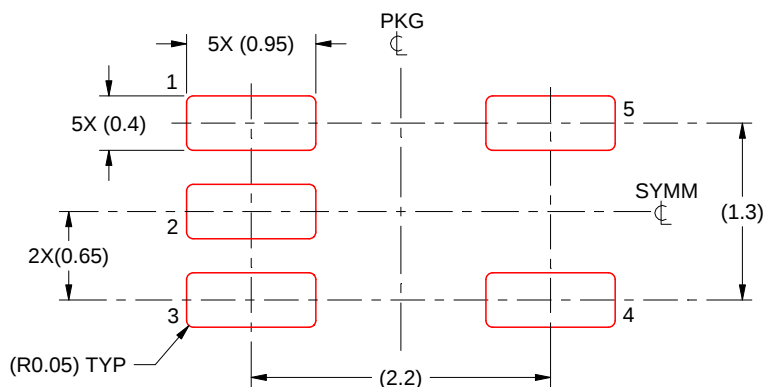


SOLDER MASK DETAILS

4214834/G 11/2024

NOTES: (continued)

7. Publication IPC-7351 may have alternate designs.
8. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SOLDER PASTE EXAMPLE  
BASED ON 0.125 THICK STENCIL  
SCALE:18X

4214834/G 11/2024

NOTES: (continued)

9. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
10. Board assembly site may have different recommendations for stencil design.

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2025, Texas Instruments Incorporated