TEXAS INSTRUMENTS

AMC7932 32-Channel, 12-Bit Analog Monitor and Controller With Multichannel ADC, Bipolar DACs, Temperature Sensor and GPIO Ports

1 Features

- 32 monotonic 12-bit DACs
  - Programmable ranges: –10 V to 0 V, –5 V to 0 V, 0 V to 5 V, and 0 V to 10 V
  - High current drive capability
  - Autorange detector
- FlexIO pins; ADC and GPIO configurable
  - AMC7932: 6 FlexIO pins
  - AMC7932F: 5 FlexIO pins
- 12-bit, 250-kSPS SAR ADC
  - Input ranges: 0 V to 5 V and 0 V to 2.5 V
  - Programmable out-of-range alarms
- General-purpose I/O (GPIO)
- Built-in sequencing features
- Internal 2.5-V reference
- Internal temperature sensor
  - Accuracy: ±2.5°C (maximum)
  - Resolution: 0.0625°C
- SPI-compatible interface: 1.65-V to 5.5-V operation
  - AMC7932: 3-wire mode
  - AMC7932F: 4-wire mode
- Specified temperature range: –40°C to +125°C
- Operating temperature range: –40°C to +150°C

2 Applications

- Macro remote radio unit (RRU)
- Active antenna system mMIMO (AAS)
- Distributed antenna systems (DAS)
- Outdoor backhaul unit
- Radar

3 Description

The AMC7932 is a highly integrated analog monitor and control device designed for high-density, general-purpose monitor and control systems. The AMC7932 includes 32 12-bit, digital-to-analog converters (DACs) with programmable output ranges. The device also incorporates a multiplexed, 12-bit analog-to-digital converter (ADC) with programmable threshold detectors, a temperature sensor, and an internal reference. The AMC7932 high level of integration significantly reduces component count and simplifies closed-loop system designs, thus making it a great choice for high-density applications where board space is critical.

The device includes flexible input/output (FlexIO) pins that can be configured as either analog inputs to the ADC or as GPIOs with two available options: AMC7932 (six FlexIO pins) and AMC7932F (five FlexIO pins). Communication to the device is performed through a 3-wire (AMC7932) or 4-wire (AMC7932F) SPI-compatible interface.

The AMC7932 high-integration and wide operating temperature range make the device an excellent choice as an all-in-one, bias-control circuit for the power amplifiers (PA) found in multichannel RF communication systems. The flexible DAC output ranges and built-in sequencing features allow the device to be used as a biasing controller for a large variety of transistor technologies, such as LDMOS, GaAs, and GaN.

Device Information

PART NUMBER	PACKAGE[(1)]	BODY SIZE (NOM)
AMC7932	TQFP (48)	7.00 mm × 7.00 mm

(1) For all available packages, see the package option addendum at the end of the data sheet.
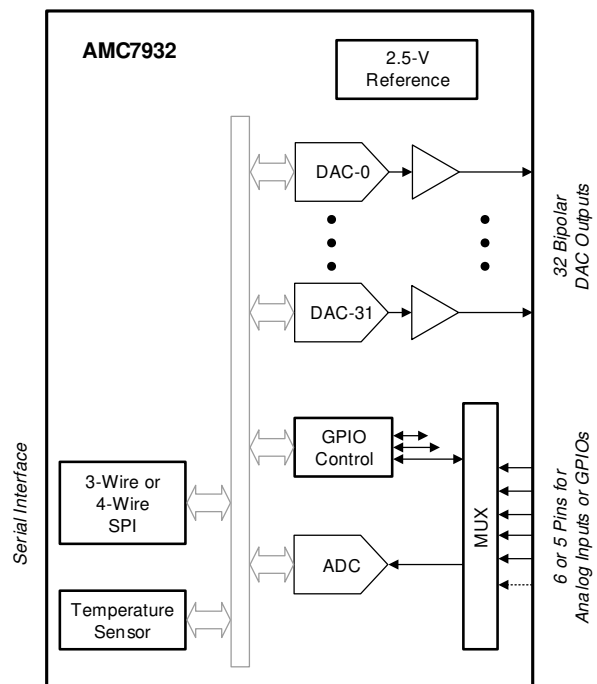
Simplified Schematic

4 Device and Documentation Support

4.1 Documentation Support

4.1.1 Related Documentation

For related documentation see the following:
- Texas Instruments, *LMP8480 / LMP8481 Precision 76V High-Side Current Sense Amplifiers with Voltage Output* data sheet
- Texas Instruments, *LM50/LM50-Q1 SOT-23 Single-Supply Centigrade Temperature Sensor* data sheet

4.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. Click on *Subscribe to updates* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

4.3 Support Resources

TI E2E™ support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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4.4 Trademarks

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All trademarks are the property of their respective owners.

4.5 Electrostatic Discharge Caution

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

4.6 Glossary

TI Glossary   This glossary lists and explains terms, acronyms, and definitions.

5 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

2   *Submit Document Feedback*

Product Folder Links: *AMC7932*

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
AMC7932FPHPR	Active	Production	HTQFP (PHP) | 48	1000 | LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932F
AMC7932FPHPR.A	Active	Production	HTQFP (PHP) | 48	1000 | LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932F
AMC7932FPHPT	Active	Production	HTQFP (PHP) | 48	250 | SMALL T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932F
AMC7932FPHPT.A	Active	Production	HTQFP (PHP) | 48	250 | SMALL T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932F
AMC7932FPHPTG4	Active	Production	HTQFP (PHP) | 48	250 | SMALL T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932F
AMC7932FPHPTG4.A	Active	Production	HTQFP (PHP) | 48	250 | SMALL T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932F
AMC7932PHPR	Active	Production	HTQFP (PHP) | 48	1000 | LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932
AMC7932PHPR.A	Active	Production	HTQFP (PHP) | 48	1000 | LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932
AMC7932PHPT	Active	Production	HTQFP (PHP) | 48	250 | SMALL T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932
AMC7932PHPT.A	Active	Production	HTQFP (PHP) | 48	250 | SMALL T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC7932

(1) Status: For more details on status, see our product life cycle.

(2) Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

(4) Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

(5) MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative

and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

![Texas Instruments logo]

PACKAGE MATERIALS INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSIONS

A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
AMC7932FPHPR	HTQFP	PHP	48	1000	330.0	16.4	9.6	9.6	1.5	12.0	16.0	Q2
AMC7932PHPR	HTQFP	PHP	48	1000	330.0	16.4	9.6	9.6	1.5	12.0	16.0	Q2

TAPE AND REEL BOX DIMENSIONS

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
AMC7932FPHPR	HTQFP	PHP	48	1000	350.0	350.0	43.0
AMC7932PHPR	HTQFP	PHP	48	1000	350.0	350.0	43.0

PHP 48

7 x 7, 0.5 mm pitch

This image is a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.
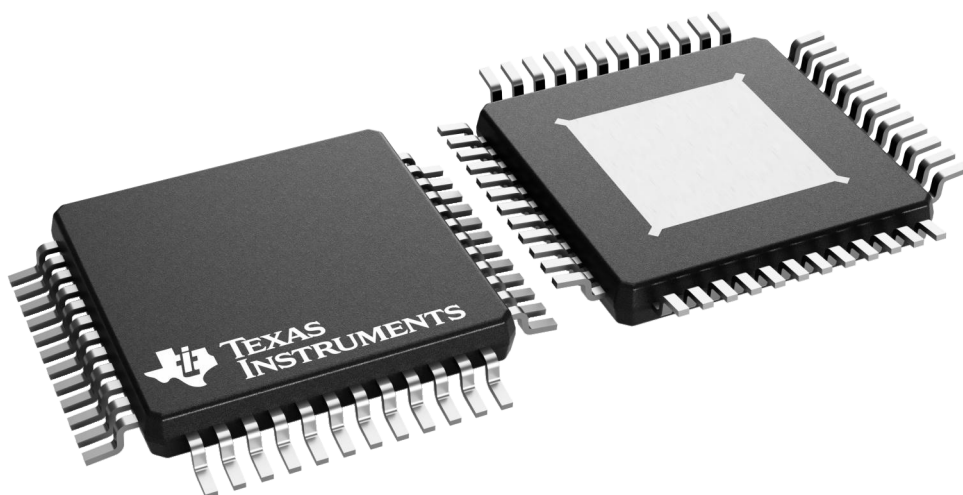
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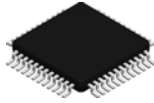
7.2
6.8
NOTE 3

B

48   37

PIN 1 ID

1   36

7.2
6.8
NOTE 3

9.2 TYP
8.8

12   25

A   13   24

44X 0.5

4X 5.5

48X 0.27
0.17

⊕ 0.08 Ⓜ C A B

1.2 MAX

C

SEATING PLANE

(0.13)
TYP

SEE DETAIL A

⌓ 0.08

13   24

12   25

5.17
4.40

49

1   36

48   37

5.17   4X (0.109) NOTE 5
4.40

0.25

GAGE PLANE

(1)

0.75
0.45

0°-7°

0.15
0.05

DETAIL A
TYPICAL
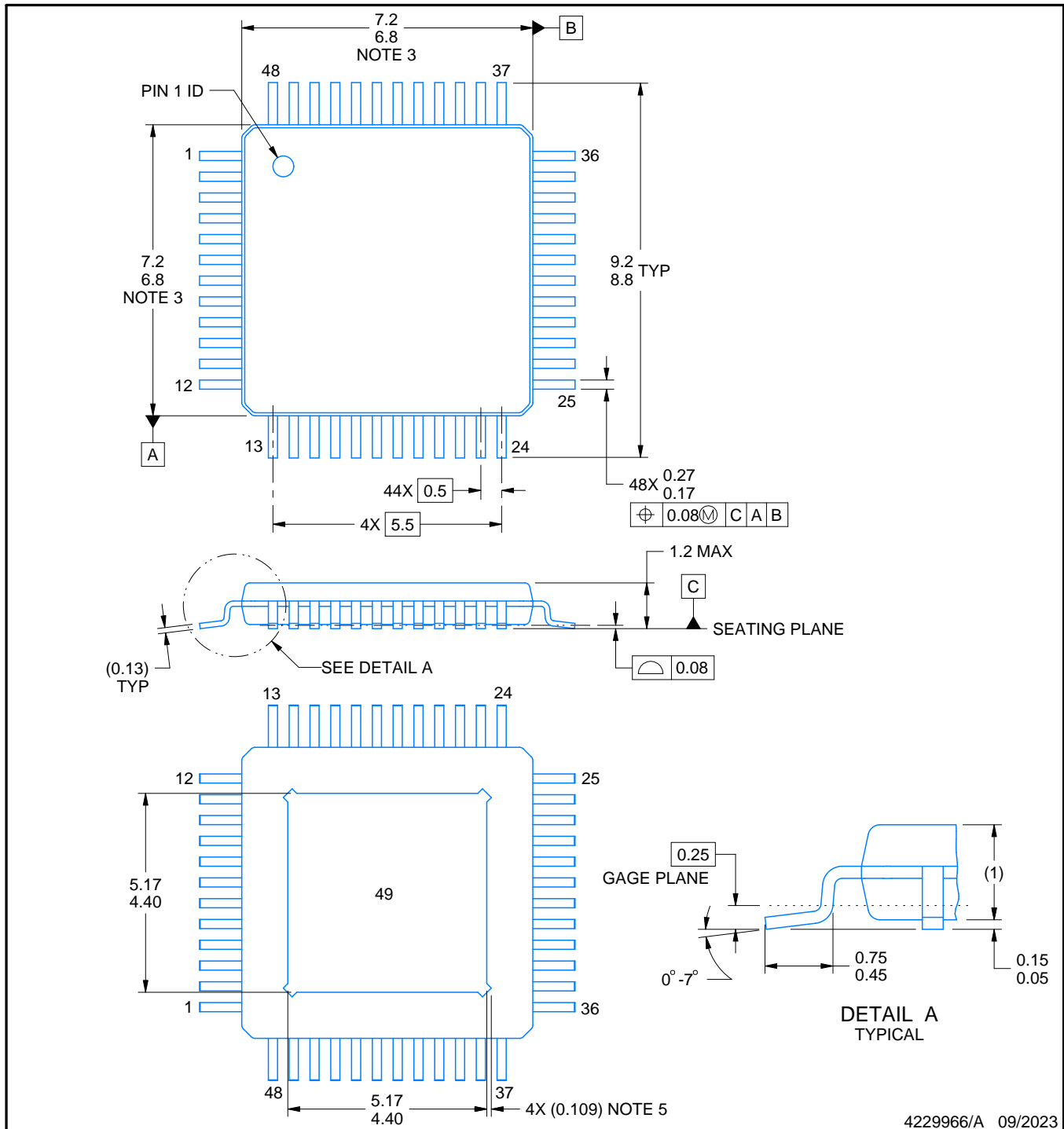
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PowerPAD is a trademark of Texas Instruments.

NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. Reference JEDEC registration MS-026.
5. Feature may not be present.

TEXAS INSTRUMENTS
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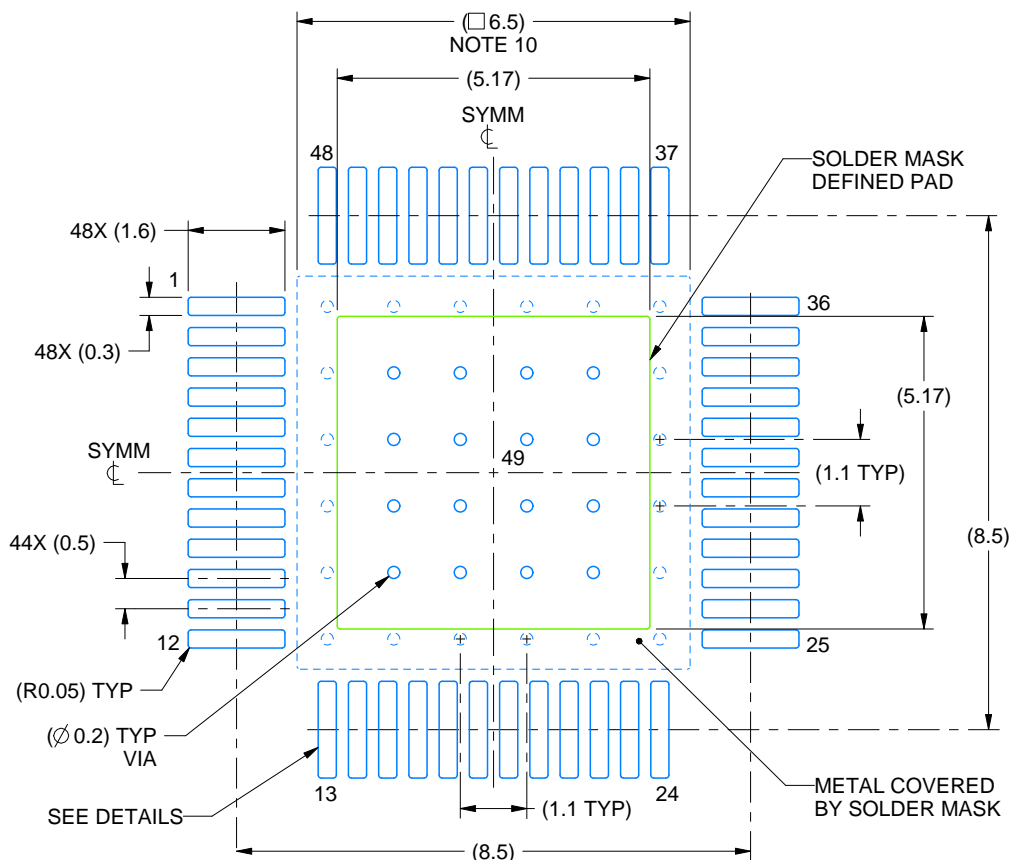
LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE:8X

SOLDER MASK DETAILS

NON SOLDER MASK DEFINED
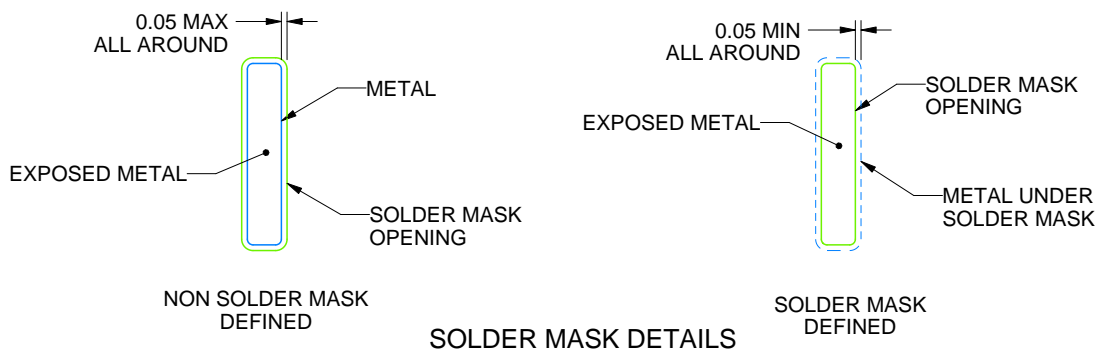
SOLDER MASK DEFINED

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NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
8. This package is designed to be soldered to a thermal pad on the board. See technical brief, Powerpad thermally enhanced package, Texas Instruments Literature No. SLMA002 (www.ti.com/lit/slma002) and SLMA004 (www.ti.com/lit/slma004).
9. Vias are optional depending on application, refer to device data sheet. It is recommended that vias under paste be filled, plugged or tented.
10. Size of metal pad may vary due to creepage requirement.
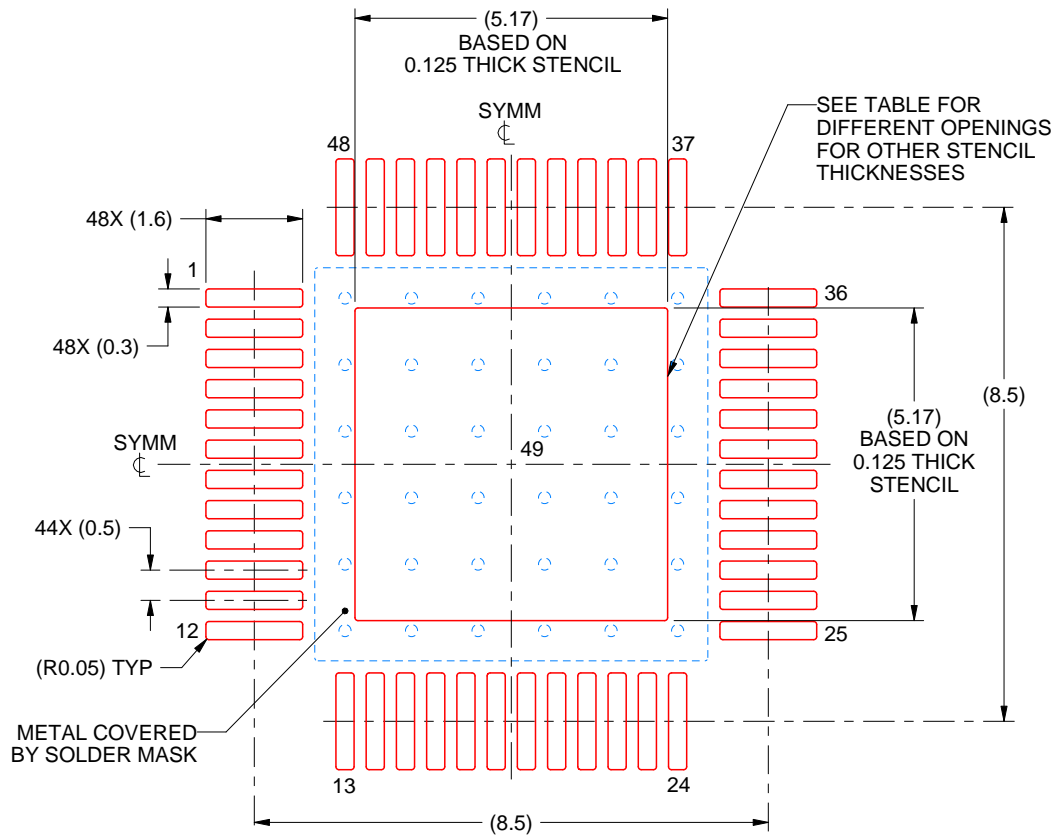
SOLDER PASTE EXAMPLE
EXPOSED PAD
100% PRINTED SOLDER COVERAGE BY AREA
SCALE:8X

STENCIL THICKNESS	SOLDER STENCIL OPENING
0.1	5.78 X 5.78
0.125	5.17 X 5.17 (SHOWN)
0.150	4.72 X 4.72
0.175	4.37 X 4.37

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NOTES: (continued)

11. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
12. Board assembly site may have different recommendations for stencil design.