

# AMC67048 DAC and ADC Analog Monitor and Supervisor

#### 1 Features

Eight monotonic 12-bit DACs

Output range: 0V to 5V

High current-drive capability

Up to 40-channel, 12-bit SAR ADC

High sample rate: 250kSPS

Input ranges: 0V to 5V and 0V to 2.5V

Programmable out-of-range alarms

**Guard logic** 

- Six FlexIO pins

 Programmable threshold detector with hysteresis

Sequencing features

Internal 2.5V reference

Internal temperature sensor

Accuracy: ±2.5°C, maximum

Resolution: 0.0625°C

Out of-range temperature alarm

Selectable SPI and I<sup>2</sup>C interfaces: 1.65V to 5.5V

Specified temperature range: -40°C to +125°C

Operating temperature range: -40°C to +150°C

# 2 Applications

Data center and enterprise computing

## 3 Description

The AMC67048 is a highly integrated analog systemguard designed for high-density, general-purpose monitor-control systems, telemetry, and redundancy safety. The AMC67048 has a 12-bit, 250kSPS analogto-digital converter (ADC) with up to 40 channels. The ADC has a programmable output range from either 0V to 2.5V or 0V to 5V. The device incorporates eight, 12-bit digital-to-analog converters (DACs) with a high-current drive capable of sourcing and sinking up to 100mA. In addition, the device contains FLEXIO pins that can either be device inputs or outputs, and are part of the device guard logic. The guard logic is designed to simplify monitoring through multiple alarms, out-of-bound ADC monitoring, and sequencing. The FLEXIO and DAC pins can be used as additional ADC channels.

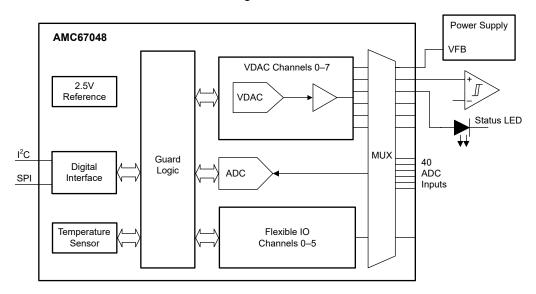
Communication to the device is done through an autodetected, 4-wire SPI-compatible or 2-wire I<sup>2</sup>C interface.

The AMC67048 high channel density, guard features, wide operating temperature range, and guard logic make this device an excellent choice for system monitoring and redundancy safety monitoring in applications such as servers and avionics telemetry. This device is designed to work independently through the integrated alarm detection or work with an MCU to provide all-in-one system monitoring.

**Package Information** 

PART NUMBER	PACKAGE <sup>(1)</sup>	PACKAGE SIZE <sup>(2)</sup>			
AMC67048	PAP (HTQFP, 64)	12mm × 12mm			

- (1) For more information, see Section 6.
- The package size (length × width) is a nominal value and includes pins, where applicable.



**Simplified Schematic** 



## 4 Device and Documentation Support

### 4.1 Documentation Support

#### 4.1.1 Related Documentation

For related documentation see the following:

Texas Instruments, AMC67048EVM User's Guide

### 4.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. Click on *Notifications* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

## 4.3 Support Resources

TI E2E<sup>™</sup> support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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#### 4.4 Trademarks

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#### 4.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 4.6 Glossary

TI Glossary

This glossary lists and explains terms, acronyms, and definitions.

### **5 Revision History**

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

DATE	REVISION	NOTES				
September 2025	*	Initial Release				

# 6 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

Product Folder Links: AMC67048

www.ti.com 4-Nov-2025

#### PACKAGING INFORMATION

Orderable part number	Status	Material type	Package   Pins	Package qty   Carrier	RoHS	Lead finish/ Ball material			Part marking (6)
						(4)	(5)		
AMC67048PAPR	Active	Production	HTQFP (PAP)   64	1000   LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	AMC67048

<sup>(1)</sup> Status: For more details on status, see our product life cycle.

- (3) RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.
- (4) Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.
- (5) MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.
- (6) Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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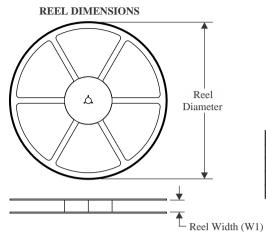
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<sup>(2)</sup> Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

# **PACKAGE MATERIALS INFORMATION**

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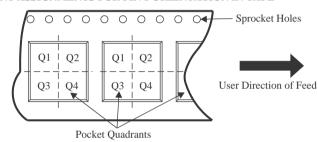
## TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

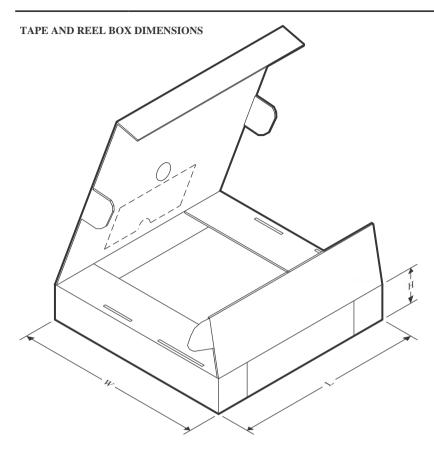


#### \*All dimensions are nominal

	Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
L	AMC67048PAPR	HTQFP	PAP	64	1000	330.0	24.4	13.0	13.0	1.5	16.0	24.0	Q2

# **PACKAGE MATERIALS INFORMATION**

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### \*All dimensions are nominal

Γ	Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)	
Г	AMC67048PAPR	HTQFP	PAP	64	1000	367.0	367.0	55.0	

10 x 10, 0.5 mm pitch

QUAD FLATPACK

This image is a representation of the package family, actual package may vary. Refer to the product data sheet for package details.



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